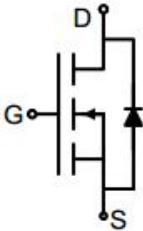
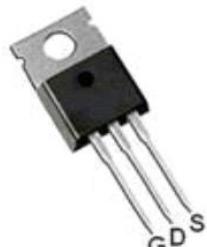


## N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The G70N04T uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> 40V</li> <li>● <math>I_D</math> (at <math>V_{GS} = 10V</math>) 70A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 10V</math>) &lt; 7mΩ</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 4.5V</math>) &lt; 9mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	 <p>Schematic diagram</p>  <p>TO-220</p>
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<b>Ordering Information</b>			
<b>Device</b>	<b>Package</b>	<b>Marking</b>	<b>Packaging</b>
G70N04T	TO-220	G70N04	50psc/Tube

<b>Absolute Maximum Ratings</b> $T_C = 25^\circ\text{C}$ , unless otherwise noted			
<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Drain-Source Voltage	$V_{DS}$	40	V
Continuous Drain Current	$I_D$	70	A
Pulsed Drain Current (note1)	$I_{DM}$	280	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Power Dissipation	$P_D$	104	W
Single pulse avalanche energy (note2)	$E_{AS}$	56	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	°C

<b>Thermal Resistance</b>			
<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	15	°C/W
Maximum Junction-to-Case	$R_{thJC}$	1.2	°C/W

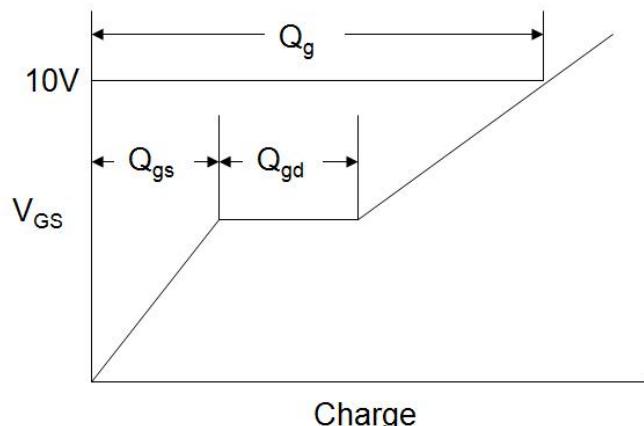
**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	40	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 40\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.1	1.7	2.4	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 30\text{A}$	--	6	7	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 20\text{A}$	--	7.5	9	
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{GS}} = 5\text{V}, I_D = 30\text{A}$	--	89	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 20\text{V}, f = 1.0\text{MHz}$	--	2169	--	pF
Output Capacitance	$C_{\text{oss}}$		--	179	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	159	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 20\text{V}, I_D = 30\text{A}, V_{\text{GS}} = 10\text{V}$	--	50	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	12	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	13	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 20\text{V}, I_D = 30\text{A}, R_G = 3\Omega$	--	11	--	ns
Turn-on Rise Time	$t_r$		--	10	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	38	--	
Turn-off Fall Time	$t_f$		--	11	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	70	A
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 30\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V

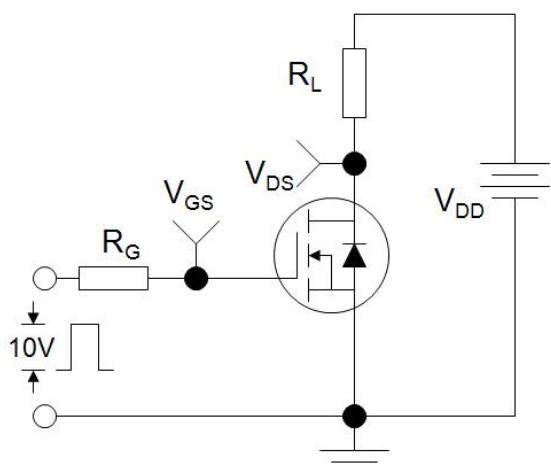
**Notes**

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{\text{DD}}=40\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_G=25\Omega$
3. Identical low side and high side switch with identical  $R_G$

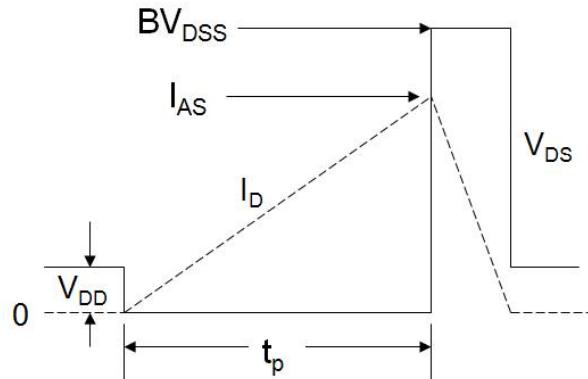
### Gate Charge Test Circuit



### Switch Time Test Circuit

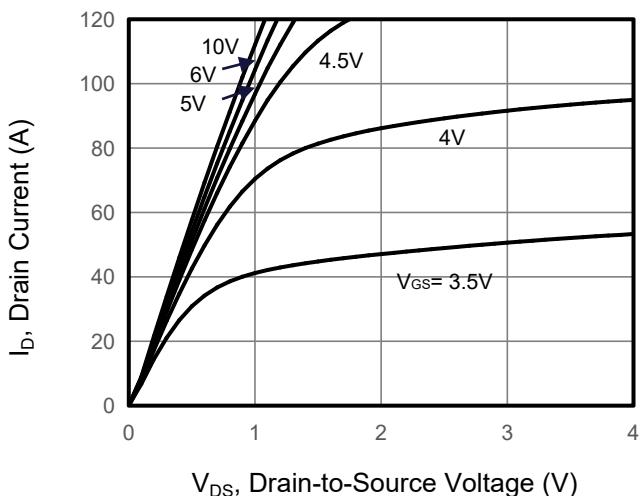


### EAS Test Circuit

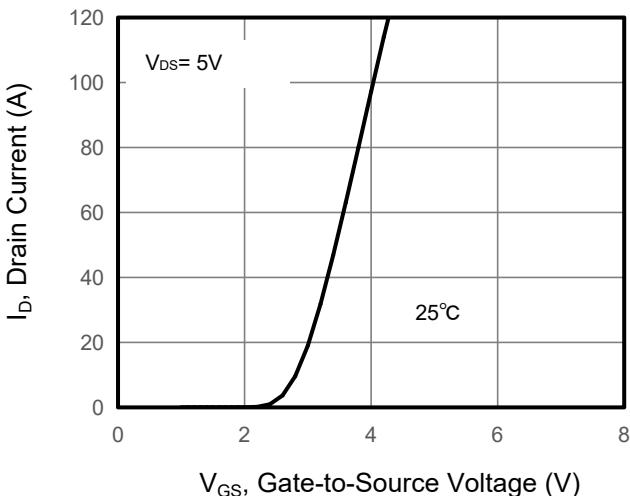


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

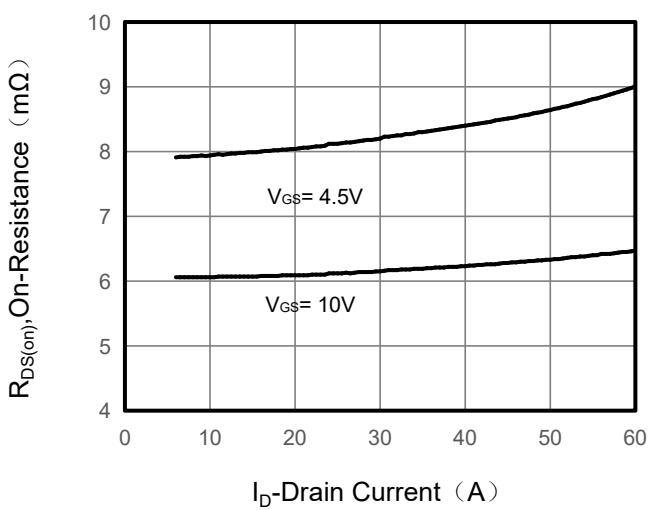
**Figure 1. Output Characteristics**



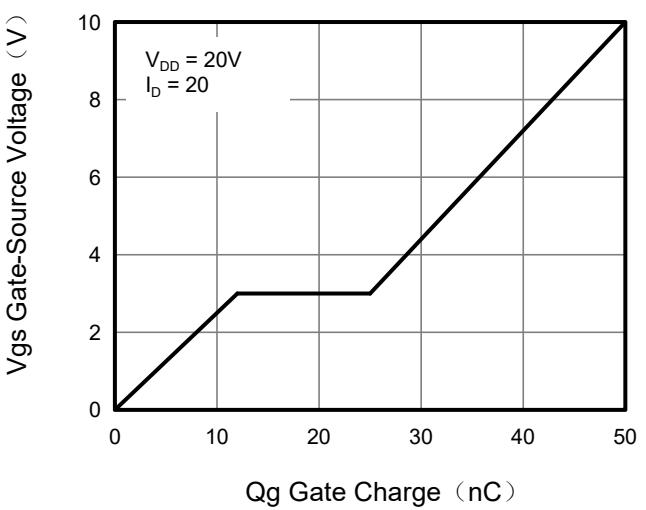
**Figure 2. Transfer Characteristics**



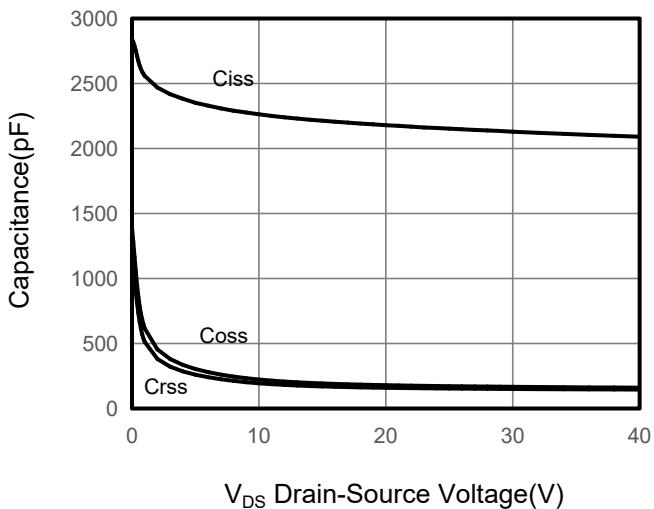
**Figure 3. Drain Source On Resistance**



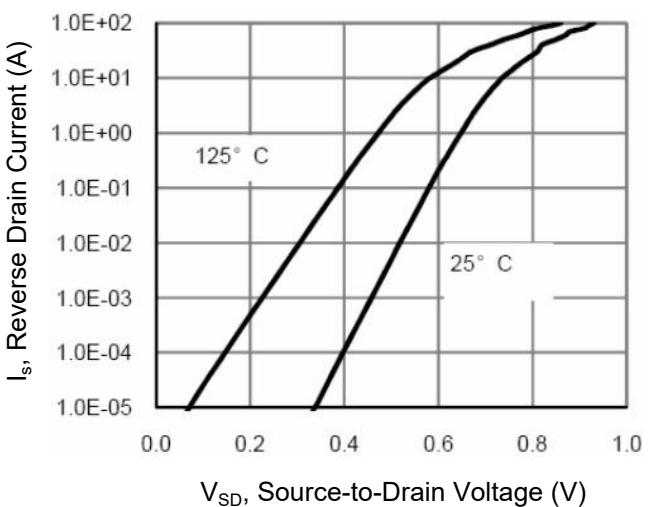
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

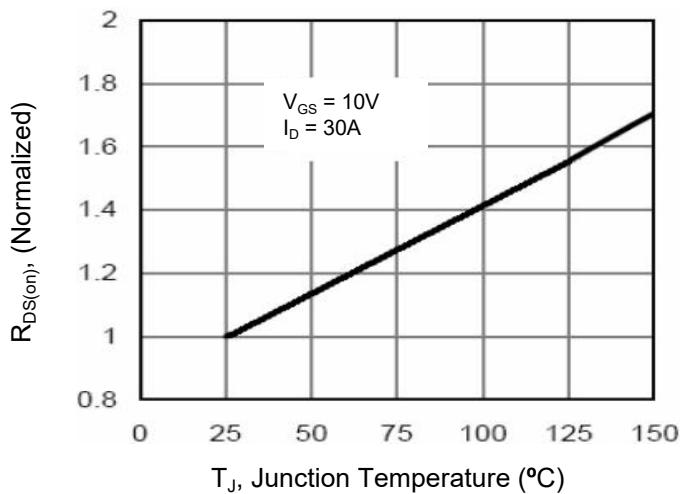


**Figure 6. Source-Drain Diode Forward**

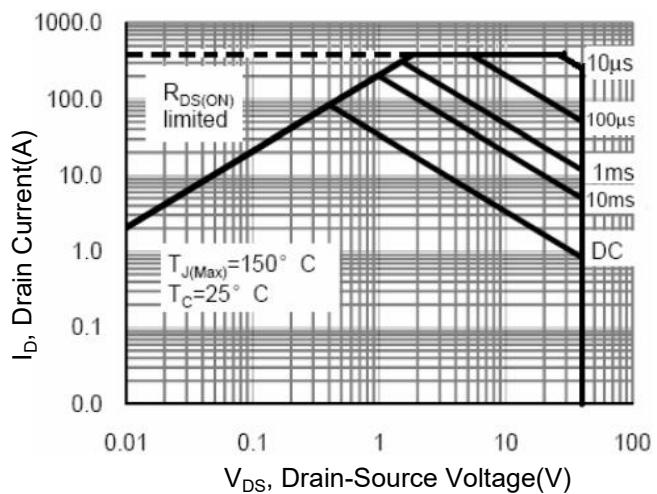


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

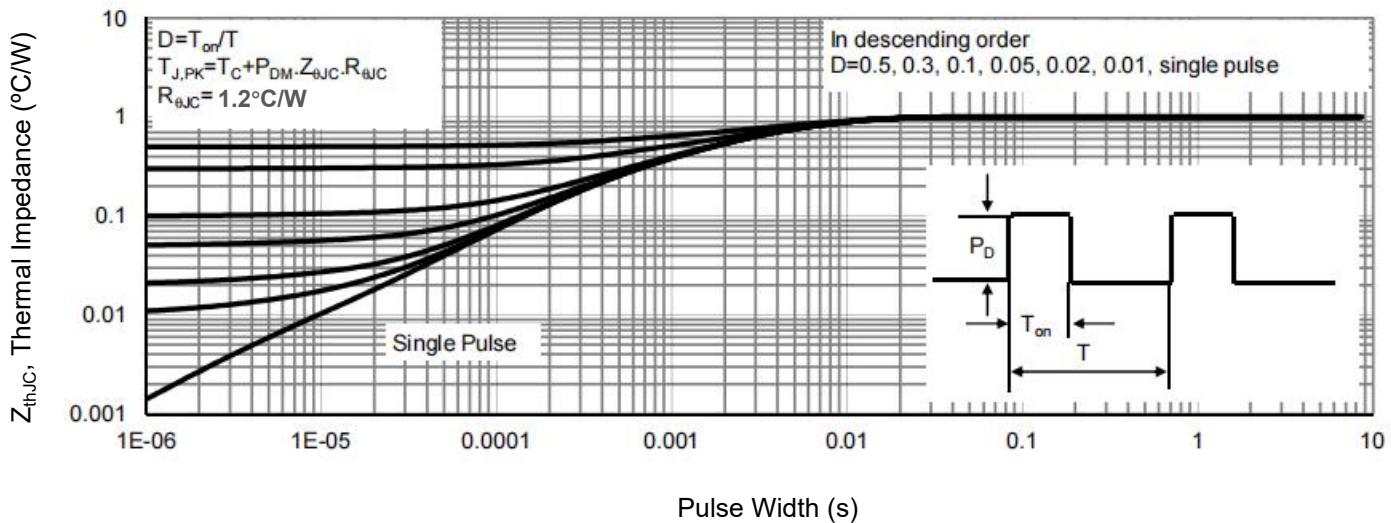
**Figure 7. Drain-Source On-Resistance**



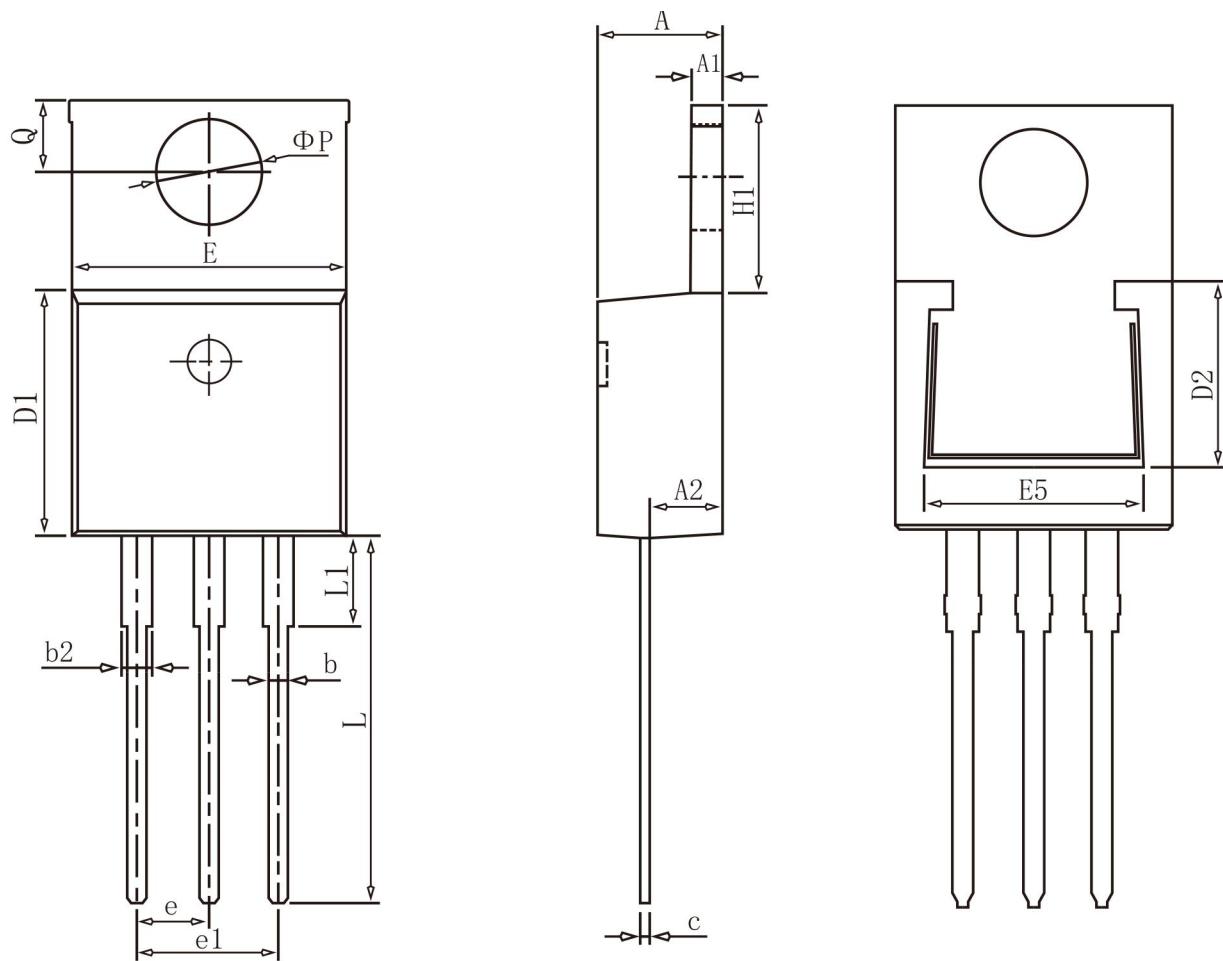
**Figure 8. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



## TO-220 Package Information



## COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	4.37	4.57	4.77
A1	1.22	1.27	1.42
A2	2.49	2.69	2.89
b	0.75	0.81	0.96
b2	1.22	1.27	1.47
c	0.30	0.38	0.48
D1	8.50	8.70	8.90
D2	5.20	-	-
E	9.86	10.16	10.36
E5	7.06	-	-
e	2.54BSC		
e1	5.08BSC		
H1	6.10	6.30	6.50
L	13.10	13.40	13.70
L1	-	3.75	4.10
Φ P	3.70	3.84	3.99
Q	2.54	2.74	2.94