Switch-mode Power Rectifiers

NHP820LFS, NRVHP820LFS

This LFPAK ultrafast rectifier provides fast switching performance with soft recovery in a compact thermally efficient package. The LFPAK package provides an excellent alternative to the DPAK, offering thermal performance nearly as good in a package occupying less than half the board space. Its low profile makes it a good option for flat panel display and other applications with limited vertical clearance. The device offers low leakage over temperature making it a good match for applications requiring low quiescent current.

Features

- New Package Provides Capability of Inspection and Probe After Board Mounting
- Low Forward Voltage Drop
- 175°C Operating Junction Temperature
- Excellent Ability to Absorb Stresses Associated with Power Temperature Cycling
- NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Mechanical Characteristics:

- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94-0 @ 0.125 in.
- Lead Finish: 100% Matte Sn (Tin)
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL 1 Requirements

Applications

- Excellent Alternative to DPAK in Space-Constrained Automotive Applications
- Very Low Leakage for Higher Temperature Operation
- Output Rectification in Compact Portable Consumer Applications

1

• Freewheeling Diode used with Inductive Loads



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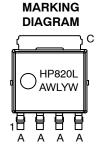
www.onsemi.com

ULTRAFAST RECTIFIERS 8 AMPERES 200 VOLTS

1,2,3,4 0 5



LFPAK4 CASE 760AB



HP820L = Specific Device Code A = Assembly Location

WL = Wafer Lot Y = Year W = Work Week

ORDERING INFORMATION

| Device | Package | Shipping† |
|----------------|---------------------|-----------------------|
| NHP820LFST1G | LFPAK4 (Pb-Free) | 3000 / Tape & Reel |
| NRVHP820LFST1G | LFPAK4 (Pb-Free) | 3000 / Tape & Reel |

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure. BRD8011/D.

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|---|--|-------------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 200 | V |
| Average Rectified Forward Current (Rated V _R , T _C = 168°C) | I _{F(AV)} | 8.0 | A |
| Peak Repetitive Forward Current, (Rated V _R , Square Wave, 20 kHz, T _C = 158°C) | I _{FRM} | 16 | А |
| Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz) | I _{FSM} | 175 | А |
| Storage Temperature Range | T _{stg} | -65 to +175 | °C |
| Operating Junction Temperature | T_J | -55 to +175 | °C |
| ESD Rating (Human Body Model) | | 3B | |
| ESD Rating (Machine Model) | | C5 | |
| Controlled Avalanche Energy (See Test Circuit in Figures 9 & 10) | W _{AVAL} | 50 | mJ |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|---|----------------|------|------|
| Thermal Resistance, Junction-to-Ambient, Steady State (Assumes 645 mm² 2 oz. copper bond pad, on a FR4 board) | $R_{	hetaJA}$ | 44 | °C/W |
| Thermal Resistance, Junction-to-Case, Steady State (Assumes 645 mm² 2 oz. copper bond pad, on a FR4 board) | $R_{	heta JC}$ | 1.07 | °C/W |

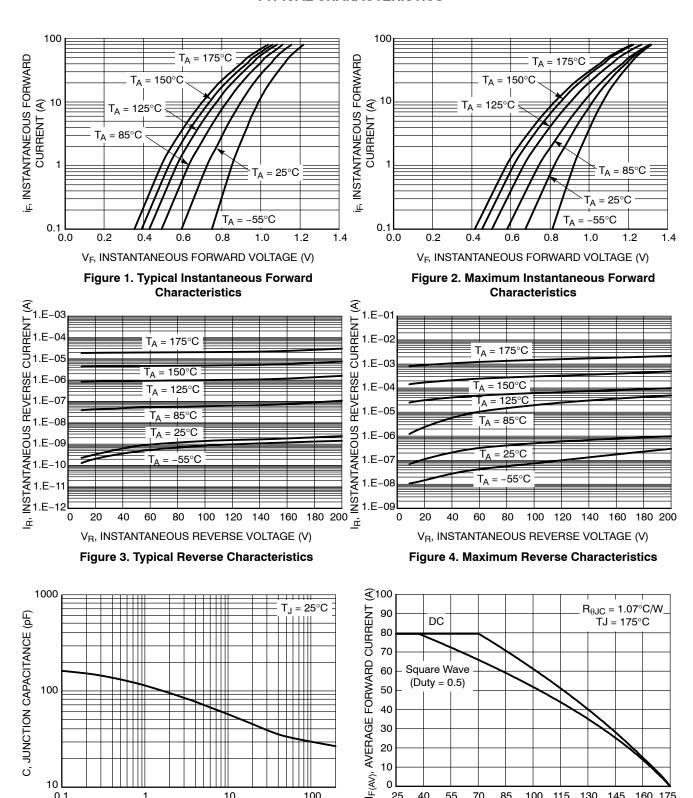
ELECTRICAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|---|-----------------|--------------|------|
| Instantaneous Forward Voltage (Note 1) | VF | 0.88 1.00 | V |
| Instantaneous Reverse Current (Note 1) (Rated dc Voltage, T _J = 125°C) (Rated dc Voltage, T _J = 25°C) | i _R | 100 1.0 | μΑ |
| Maximum Reverse Recovery Time (I _F = 1.0 A, di/dt = 50 A/ μ s, V _R = 30 V) | T _{rr} | 35 | ns |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width = $300 \mu s$, Duty Cycle $\leq 2.0\%$.

TYPICAL CHARACTERISTICS



V_R, REVERSE VOLTAGE (V) Figure 5. Typical Junction Capacitance

0.1

10

T_C, CASE TEMPERATURE (°C) Figure 6. Current Derating per Device

100 115 130

145

85

25 40 55

100

TYPICAL CHARACTERISTICS

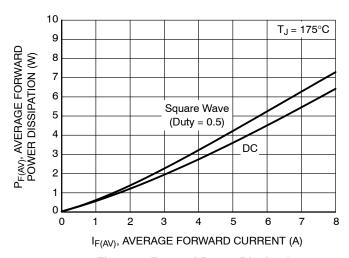


Figure 7. Forward Power Dissipation

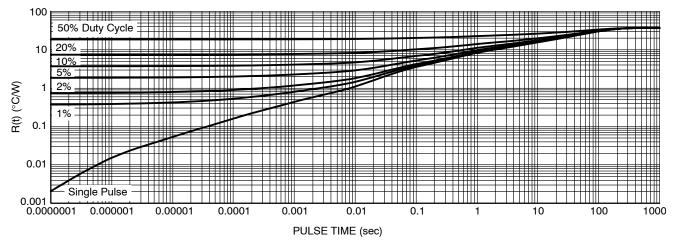


Figure 8. Typical Thermal Characteristics, Junction-to-Ambient

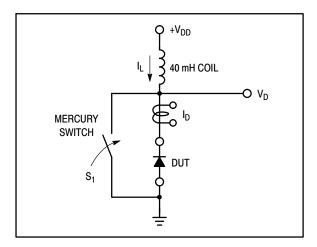


Figure 9. Test Circuit

The unclamped inductive switching circuit shown in Figure 9 was used to demonstrate the controlled avalanche capability of the new "E" series Ultrafast rectifiers. A mercury switch was used instead of an electronic switch to simulate a noisy environment when the switch was being opened.

When S_1 is closed at t_0 the current in the inductor I_L ramps up linearly; and energy is stored in the coil. At t_1 the switch is opened and the voltage across the diode under test begins to rise rapidly, due to di/dt effects, when this induced voltage reaches the breakdown voltage of the diode, it is clamped at BV_{DUT} and the diode begins to conduct the full load current which now starts to decay linearly through the diode, and goes to zero at t_2 .

By solving the loop equation at the point in time when S_1 is opened; and calculating the energy that is transferred to the diode it can be shown that the total energy transferred is equal to the energy stored in the inductor plus a finite amount of energy from the V_{DD} power supply while the diode is in breakdown (from t_1 to t_2) minus any losses due to finite

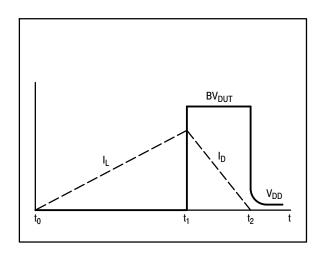


Figure 10. Current-Voltage Waveforms

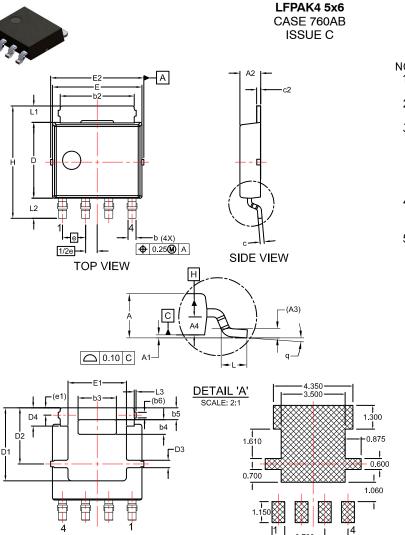
component resistances. Assuming the component resistive elements are small Equation (1) approximates the total energy transferred to the diode. It can be seen from this equation that if the V_{DD} voltage is low compared to the breakdown voltage of the device, the amount of energy contributed by the supply during breakdown is small and the total energy can be assumed to be nearly equal to the energy stored in the coil during the time when S_1 was closed, Equation (2).

EQUATION (1):

$$W_{AVAL} \approx \frac{1}{2}LI_{LPK}^{2} \left(\frac{BV_{DUT}}{BV_{DUT} - V_{DD}} \right)$$

EQUATION (2):

$$W_{AVAL} \approx \frac{1}{2}LI_{LPK}^2$$



DATE 19 NOV 2019

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.150mm PER SIDE.
- 4. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 5. DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.

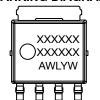
| UNIT IN MILLIMETER | | | |
|--------------------|-----------|----------|------|
| DIM | MIN | NOM | MAX |
| Α | 1.10 | 1.20 | 1.30 |
| A1 | 0.00 | 0.08 | 0.15 |
| A2 | 1.10 | 1.15 | 1.20 |
| A3 | (|).25 REF | = |
| A4 | 0.45 | 0.50 | 0.55 |
| b | 0.40 | 0.45 | 0.50 |
| b2 | 3.80 | 4.10 | 4.40 |
| b3 | 2.00 | 2.10 | 2.20 |
| b4 | 0.70 | 0.80 | 0.90 |
| b5 | 0.55 | 0.65 | 0.75 |
| b6 | 0.31 REF | | |
| С | 0.19 | 0.22 | 0.25 |
| c2 | 0.19 | 0.22 | 0.25 |
| D | 4.05 | 4.15 | 4.25 |
| D1 | 3.80 | 4.00 | 4.20 |
| D2 | 3.00 | 3.10 | 3.20 |
| D3 | 0.30 | 0.40 | 0.50 |
| D4 | 0.90 | 1.00 | 1.10 |
| Е | 4.80 | 4.90 | 5.00 |
| E1 | 3.10 | 3.20 | 3.30 |
| E2 | 5.00 | 5.15 | 5.30 |
| е | 1.27 BSC | | |
| 1/2e | 0.635 BSC | | |
| e1 | 0,40 REF | | |
| Ι | 6.00 | 6.15 | 6.30 |
| L | 0.40 | 0.65 | 0.85 |
| L1 | 0.80 | 0.90 | 1.00 |
| L2 | 0.90 | 1.10 | 1.30 |
| L3 | 0.00 | 0.10 | 0.20 |
| q | 0° | 4° | 8° |

RECOMMENDED LAND PATTERN

*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC MARKING DIAGRAM*

BOTTOM VIEW



XXXXXX = Specific Device Code A = Assembly Location

WL = Wafer Lot Y = Year

Y = Year W = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Some products may not follow the Generic Marking.

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|------------------|-------------|---|-------------|
| DESCRIPTION: | LFPAK4 5x6 | • | PAGE 1 OF 1 |

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