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Product Change Notification - SYST-20OVGB824

Date:	21 Jun 2016
Product Category:	
Affected CPNs:	
Notification subject:	Data Sheet - PL138-48 - 2.5V-3.3V Low-Skew 1-4 Differential PECL Fanout Buffer Data Sheet Document Revision
Notification text:	SYST-20OVGB824

Microchip has released a new DeviceDoc for the PL138-48 - 2.5V-3.3V Low-Skew 1-4 Differential PECL Fanout Buffer of devices. If you are using one of these devices please read the document located at [PL138-48 - 2.5V-3.3V Low-Skew 1-4 Differential PECL Fanout Buffer](#).

Notification Status: Final

Description of Change: 1. Updated output frequency tolerances to 800 MHz

Pre Change: Output Frequency: ≤ 1 GHz

Post Change: Output Frequency: ≤ 800 MHz

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 21 Jun 2016

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s): [PL138-48 - 2.5V-3.3V Low-Skew 1-4 Differential PECL Fanout Buffer](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

PL138-48OI

PL138-48OC-R

PL138-48OC