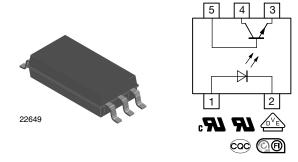


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# Optocoupler, Phototransistor Output, SOP-6L5, 110 °C Rated, Half Pitch, Long Mini-Flat Package



### **LINKS TO ADDITIONAL RESOURCES**









#### **DESCRIPTION**

The TCLT111. series consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 5-lead SOP-6L package.

### **APPLICATIONS**

- Switchmode power supplies
- · Computer peripheral interface
- Microprocessor system interface

#### **FEATURES**

- SMD low profile 5 pin package
- Isolation test voltage 5000 V<sub>RMS</sub>
- CTR flexibility available see order information
- Special construction
- Extra low coupling capacitance
- Connected base
- DC input with transistor output
- Temperature range -55 °C to 110 °C
- Creepage distance > 8 mm
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

### **AGENCY APPROVALS**

- UL
- cUL
- DIN EN 60747-5-5 (VDE 0884-5)
- FIMKO
- BSI
- CQC GB4943.1
- CQC GB8898

#### ORDERING INFORMATION SOP-6L5 1 Т С L Т 1 1 PART NUMBER **AGENCY CTR (%) CERTIFIED /** 5 mA 10 mA 5 mA **PACKAGE** UL, cUL, VDE, 100 to 200 160 to 320 100 to 300 130 to 260 200 to 400 50 to 600 40 to 80 63 to 125 50 to 150 80 to 160 FIMKO, CQC SOP-6L5 TCLT1110 TCLT1111 TCLT1112 TCLT1113 TCLT1114 TCLT1115 TCLT1116



ROHS COMPLIANT HALOGEN

FREE GREEN

<u>(5-2008)</u>



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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT				_		
Reverse voltage		$V_{R}$	6	V		
Forward current		I <sub>F</sub>	60	mA		
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	1.5	Α		
Power dissipation		P <sub>diss</sub>	100	mW		
Junction temperature		Tj	125	°C		
OUTPUT						
Collector emitter voltage		V <sub>CEO</sub>	70	V		
Emitter collector voltage		V <sub>ECO</sub>	7	V		
Collector current		I <sub>C</sub>	50	mA		
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA		
Power dissipation		P <sub>diss</sub>	150	mW		
Junction temperature		Tj	125	°C		
COUPLER						
Total power dissipation		P <sub>tot</sub>	250	mW		
Operating ambient temperature range		T <sub>amb</sub>	-55 to +110	°C		
Storage temperature range		T <sub>stg</sub>	-55 to +125	°C		
Soldering temperature (1)		T <sub>sld</sub>	260	°C		

#### Notes

- Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  Maximum Rating for extended periods of the time can adversely affect reliability.
- (1) Wave soldering three cycles are allowed. Also refer to "Assembly Instruction" (www.vishay.com/doc?80054).

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
input	input							
Forward voltage	$I_F = 50 \text{ mA}$	V <sub>F</sub>	-	1.25	1.6	V		
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	C <sub>j</sub>	-	50	-	pF		
output	output							
Collector emitter voltage	I <sub>C</sub> = 1 mA	$V_{CEO}$	80	-	-	V		
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7	-	-	V		
Collector emitter leakage current	$V_{CE} = 20 \text{ V}, I_F = 0 \text{ A}$	I <sub>CEO</sub>	-	10	100	nA		
coupler								
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V <sub>CEsat</sub>	-	-	0.3	V		
Cut-off frequency	$V_{CE}$ = 5 V, $I_F$ = 10 mA, $R_L$ = 100 $\Omega$	f <sub>c</sub>	-	110	-	kHz		
Coupling capacitance	f = 1 MHz	C <sub>k</sub>	-	0.3	-	pF		

### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.



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CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCLT1110	CTR	50	-	600	%
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 10 mA	TCLT1111	CTR	40	-	80	%
		TCLT1112	CTR	63	-	125	%
		TCLT1113	CTR	100	-	200	%
		TCLT1114	CTR	160	-	320	%
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 1 mA	TCLT1111	CTR	13	30	-	%
L- /L-		TCLT1112	CTR	22	45	-	%
I <sub>O</sub> ∕I <sub>F</sub>		TCLT1113	CTR	34	70	-	%
		TCLT1114	CTR	56	100	-	%
	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCLT1115	CTR	50	-	150	%
		TCLT1116	CTR	100	-	300	%
		TCLT1117	CTR	80	-	160	%
		TCLT1118	CTR	130	-	260	%
		TCLT1119	CTR	200	-	400	%

SAFETY AND INSULATION RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Tested withstanding isolation voltage	According to UL1577, t = 1 s	V <sub>ISO</sub>	5000	V <sub>RMS</sub>
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V <sub>IORM</sub>	890	V <sub>peak</sub>
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s}, \text{ (see figure 2)}$	$V_{pd}$	13 000	V
Isolation resistance	$T_{amb} = 25  ^{\circ}\text{C},  V_{IO} = 500  \text{V}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
	T <sub>amb</sub> = 100 °C, V <sub>IO</sub> = 500 V	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
	T <sub>amb</sub> = 150 °C, V <sub>IO</sub> = 500 V (construction test only)	R <sub>IO</sub>	≥ 10 <sup>9</sup>	Ω
Output safety power		P <sub>SO</sub>	265	mW
Input safety current		I <sub>SI</sub>	130	mA
Input safety temperature		T <sub>S</sub>	150	°C
Creepage distance	DIP-6, option 6		≥ 8	mm
Clearance distance	DIP-6, option 6		≥ 8	mm
Insulation distance (internal)			0.75	mm

### Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

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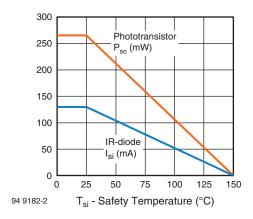


Fig. 1 - Derating Diagram

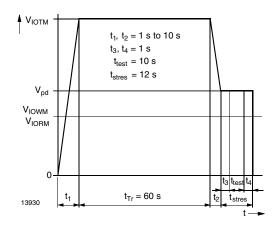


Fig. 2 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-5 (VDE 0884-5); IEC 60747-5-5

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t <sub>d</sub>	-	3	-	μs
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t <sub>r</sub>	-	3	-	μs
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t <sub>f</sub>	-	4.7	-	μs
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t <sub>s</sub>	-	0.3	-	μs
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t <sub>on</sub>	-	6	-	μs
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t <sub>off</sub>	-	5	-	μs
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega, \text{ (see Fig. 4)}$	t <sub>on</sub>	-	9	-	μs
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_I = 1 \text{ k}\Omega, \text{ (see Fig. 4)}$	t <sub>off</sub>	-	10	-	μs

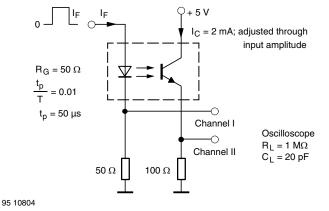


Fig. 3 - Test Circuit, Non-Saturated Operation

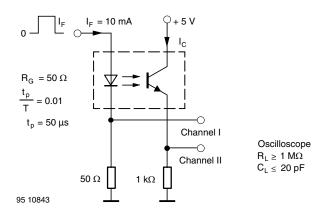


Fig. 4 - Test Circuit, Saturated Operation

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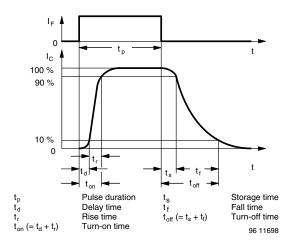
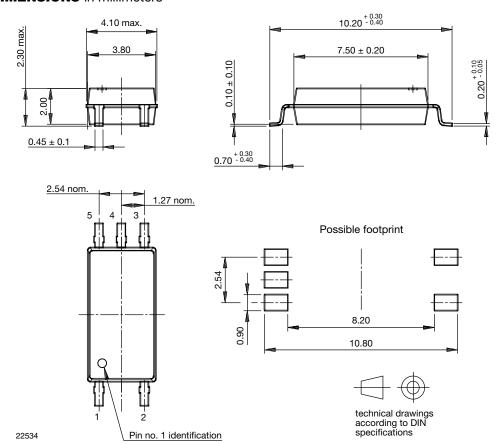
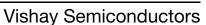


Fig. 5 - Switching Times

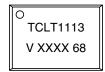
### **PACKAGE DIMENSIONS** in millimeters







### **PACKAGE MARKING**



#### Note

• XXXX = LMC (lot marking code)

### TAPE AND REEL DIMENSIONS (in millimeters)

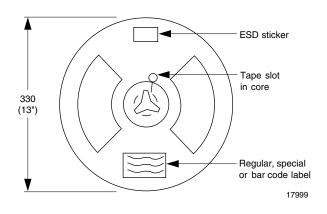
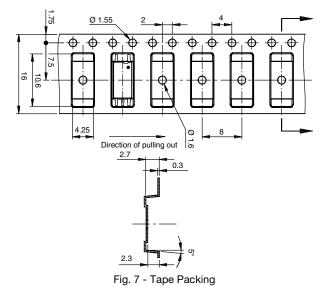


Fig. 6 - Reel Dimensions



### **SOLDER PROFILE**

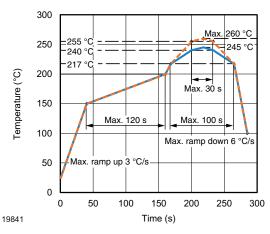


Fig. 8 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020

### **HANDLING AND STORAGE CONDITIONS**

ESD level: HBM class 2 Floor life: unlimited

Conditions: T<sub>amb</sub> < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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