

HSSC424.xxx - 0402 High Stability Silicon Capacitor

Rev 3.2



Key features

- Ultra high stability :
 - Temperature <±0.5% (-55 °C to +150 °C)
 - Voltage <0.1 %/V
 - Negligible aging <0.001% /1000hours
- Unique high capacitance in EIA/0402 package size, up to 100 nF
- High reliability (FIT <0.017 parts / billion hours)</p>
- Low leakage current down to 100 pA
- Low ESL and Low ESR
- Suitable with lead free reflow-soldering *Please refer to our assembly Application Note for further recommendations

Thanks to the unique IPDiA Silicon capacitor technology, most of the problems encountered in demanding application can be solved.

High Stability Silicon Capacitors are dedicated to applications where **Reliability** is the main parameter thanks to our end of production Burnin.

HSSC avoid the need to oversize the capacitor value for sensitive capacitive circuitry and offers a **higher DC voltage stability**.

This technology provides industry leading performances relative to the **capacitor stability** over the full **operating voltage & temperature range.**

The very high and stable insulation resistance of silicon capacitors can enhance up to 30 % the **battery lifetime** in mobile applications.

Key applications

- All demanding applications, such as medical, aerospace, automotive industry
- High stability applications
- Decoupling / Filtering / Charge pump (i.e.: Pacemakers / defibrillators)
- Devices with battery operations
- Replacement of X7R and NP0
- Downsizing

The IPDiA technology features a capacitor integration capability (up to 250nF/mm²) which allows a **smaller case size** than existing solutions to answer high volume constraints. This technology also offers **high reliability**, up to 10 times better than alternative capacitor technologies, such as Tantalum or MLCC, and eliminates cracking phenomena.

This Silicon based technology is RoHS compliant and compatible with lead free reflow soldering process.



Value

<±0.5 %, from -55 °C to +150 °C

0.1 % /V (from 0 V to RVDC)

Negligible, < 0.001 % / 1000h

FIT<0.017 parts / billion hours

100 pF to 100 nF

-55 °C to 150 °C

- 70 °C to 165 °C

±15 %⁽

11, 30 V

Max 100 pH

Max 400m $\Omega^{(}$ 100G Ω min @ 3V, from

-55°C to +150°C

Max 400 µm (*)

Parameters

Capacitance range

Capacitance tolerances

Storage temperatures

Temperature coefficient

Breakdown voltage (BV)

Insulation resistance

Capacitor height

RVDC

Ageing Reliability

Operating temperature range

Capacitance variation versus

Equivalent Serial Inductor (ESL)

Equivalent Serial Resistor (ESR)

Electrical specification

				Capacita	acitance value				
		10	15	22	33	47	68		
Unit	1 pF	Contact IPDIA Sales	Contact IPDIA Sales						
	10 pF	100 pF: 935.131.424.310	150 pF: 935.131.424.315	220 pF: 935.131.424.322	330 pF: 935.131.424.333	470 pF: 935.131.424.347	680 pF: 935.131.424.368		
	0.1 nF	1 nF: 935.131.424.410	1.5 nF: 935.131.424.415	2.2 nF: 935.131.424.422	3.3 nF: 935.131.424.433	4.7 nF: 935.131.424.447	6.8 nF: 935.131.424.468		
	1 nF	10 nF: 935.131.424.510	15 nF: 935.131.424.515	22 nF: 935.131.424.522	33 nF: 935.131.424.533	47 nF: 935.131.424.547 935.131.724.547	Contact IPDIA Sales		
	10 nF	100 nF: 935.131.424.610							

(*) Thinner thickness (as low as 100 µm thick) available, see Low Profile Silicon Capacitor product: LPSC

(**) Extended temperature range (up to +250 °C) available, see Xtreme Temperature Silicon Capacitor product: XTSC

(***) Other values on request.



Fig.1 Capacitance change versus temperature variation compared with alternative dielectrics



variation compared with alternative dielectrics



Fig.3 ESL versus capacitance value compared with alternative dielectrics

Part Number

	<u>935.131.</u>	<u>B</u> .2	▶ <u>Breakdown</u>	<u>s.</u> ↓	¥		<u>×x</u> — •	<u>Value (E6)</u> 10 15
i.e.: 100 nF/04 → 935.131.424	402 case (HSSC ty 4.610	pe)	<u>Voltage</u> 4 = 11V 7 = 30V	<u>Size</u> 4 = 0402	$ \underbrace{\text{Unit}}{0 = 10 \text{ f}} \\ 1 = 0.1 \text{ p} \\ 2 = 1 \text{ p} \\ 3 = 10 \text{ p} \\ 4 = 0.1 \text{ n} $	$5 = 1 n 6 = 10 n 7 = 0.1 \mu 8 = 1 \mu 9 = 10 u$		22 33 47 68

Termination and Outline

Termination

Lead-free nickel/solder coating compatible with automatic soldering technologies: reflow and manual.

Typical dimensions, all dimensions in mm.

Package outline





(0402 PCB footprint)

Packaging

Tape and reel, tray, waffle pack or wafer delivery.

Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.



For more information, please visit: <u>http://www.ipdia.com</u> To contact us, email to: <u>sales@ipdia.com</u>

Date of release: 28th February 2014 Document identifier: CL431 111 615 137