

SAW Rx filter

Automotive telematics WCDMA band 5

Series/type: B4362

Ordering code: B39881B4362P810

Date: December 22, 2016

Version: 2.0

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1 Application

- Low-loss RF filter for Band 5 system, receive path (Rx)
- Suitable for diversity applications
- Usable pass band 25MHz

2 Features

- Package size 1.4±0.1 mm × 1.1±0.1 mm
- Package height 0.45 mm (max.)
- Approximate weight 3 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Filter surface passivated
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 2a (MSL2a)
- AEC-Q200 qualified component family (Grade 1: -40 °C to +125 °C)



Figure 1: Picture of component with example of product marking.



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3 Package

0.25 (0.075) (

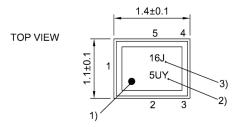
Pad and pitch tolerance ±0.05

4 Pin configuration

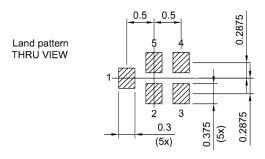
- 1 Input
- 4 Output
- 2, 3, 5 Ground

SIDE VIEW





- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number



Landing pad tolerance -0.02

Figure 2: Drawing of package with package height A = 0.45 mm (max.). See Sec. Package information (p. 18).



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5 Matching circuit

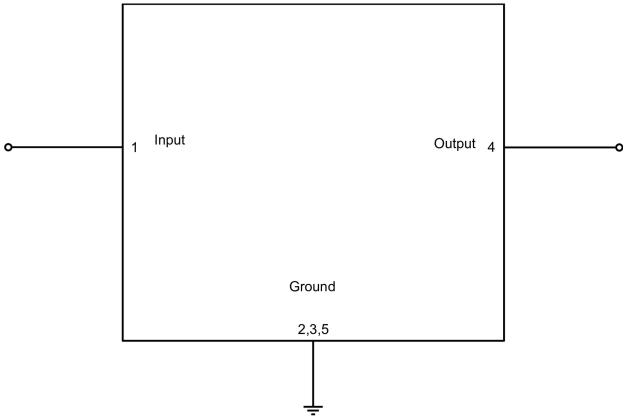


Figure 3: Schematic of matching circuit. No external matching components required.



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6 Characteristics

Temperature range for specification $T_{\text{SPEC}} = -40 \, ^{\circ}\text{C} \dots +85 \, ^{\circ}\text{C}$

Input terminating impedance $Z_{\rm IN} = 50~\Omega$ Output terminating impedance $Z_{\rm OUT} = 50~\Omega$

Characteristics					$\begin{array}{c} \textbf{min.} \\ \textbf{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{SPEC}} \end{array}$	
Center frequency				f _C	_	881.5	_	MHz
Maximum insertion attenuation								
		869 894	MHz	α_{max}	_	1.9	2.3	dB
	@f _{carrier}	871.4 891.6	MHz	$\alpha_{\text{WCDMA,max}}^{\qquad 1)}$	_	1.7	1.9	dB
Amplitude ripple (p-p)				Δα				
		869 894	MHz		_	0.6	1.1	dB
Maximum VSWR				$VSWR_{max}$				
@ input port		869 894	MHz		_	1.8	2.0	
@ output port		869 894	MHz		_	1.8	2.0	
Maximum error vector magnitude				EVM _{max} ²⁾				
		871.4 891.6	MHz		_	3.0	6.0	%
Minimum attenuation								
		50 824	MHz	$\boldsymbol{\alpha}_{min}$	46	56	_	dB
		824 849	MHz	$\boldsymbol{\alpha}_{\text{min}}$	46	53	_	dB
	@f _{carrier}	826.4 846.6	MHz	$\alpha_{\text{WCDMA,min}}^{\qquad 1)}$	50	53	_	dB
		849 854	MHz	$\boldsymbol{\alpha}_{_{min}}$	30	51	_	dB
		909 979	MHz	$\boldsymbol{\alpha}_{min}$	11	20	_	dB
		979 1710	MHz	$\alpha_{_{min}}$	30	47	_	dB
		1710 1785	MHz	$\alpha_{_{min}}$	41	47	_	dB
		1850 1910	MHz	$\alpha_{_{min}}$	40	46	_	dB
		1920 1980	MHz	$\alpha_{_{min}}$	40	44	_	dB
		2400 2500	MHz	$\boldsymbol{\alpha}_{_{min}}$	37	41	_	dB
		2607 2682	MHz	$\alpha_{_{min}}$	35	38	_	dB
		4900 5950	MHz	$\boldsymbol{\alpha}_{\text{min}}$	23	38	_	dB

Attenuation of WCDMA signal ("power transfer function"). Please refer to definition of Power Transfer Function (PTF) of WCDMA signal (p. 17).

²⁾ Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.



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Maximum ratings

Operable temperature	T _{OP} = -40 °C +125 °C	
Storage temperature	T _{STG} ¹⁾ = -40 °C +125 °C	
DC voltage	$ V_{DC} ^{2} = 0 \text{ V (max.)}$	
Input power @ input port: 824 849 MHz	P _{IN} = 15 dBm	Continuous wave for 2000 h @ 55 °C.

Not valid for packaging material. Storage temperature for packaging material is -25 °C to +40 °C. In case of applied DC voltage blocking capacitors are mandatory.



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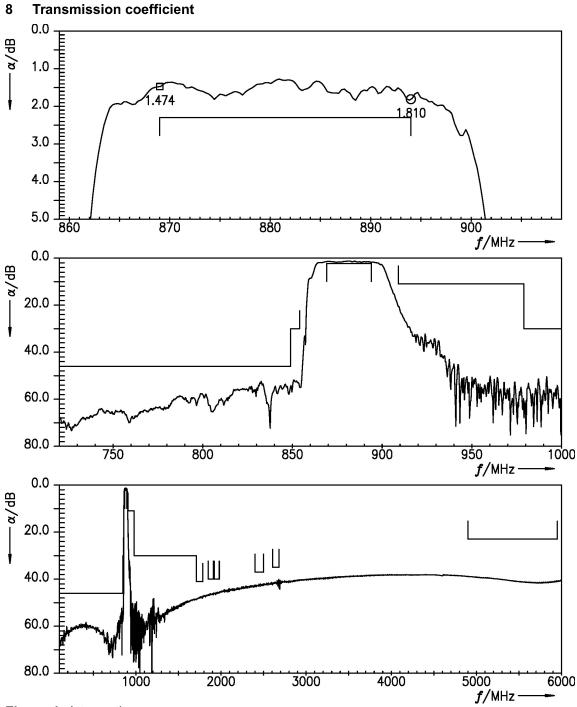


Figure 4: Attenuation.



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9 Reflection coefficients

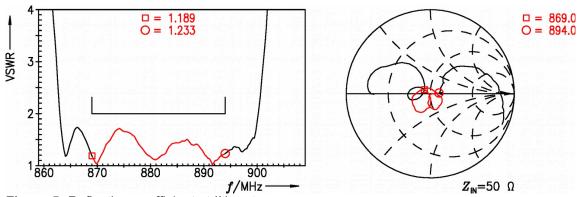


Figure 5: Reflection coefficient at IN port.

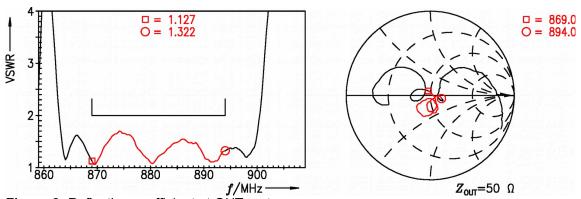


Figure 6: Reflection coefficient at OUT port.



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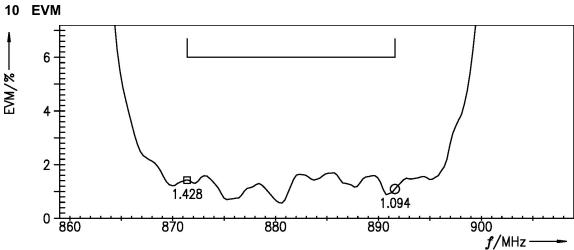


Figure 7: Error vector magnitude.



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11 Packing material

11.1 Tape

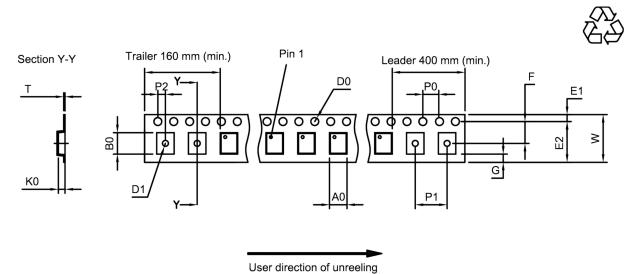


Figure 8: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A ₀	1.27±0.05 mm	 E ₂	6.25 mm (min.)		P ₁	4.0±0.1 mm
B ₀	1.57±0.05 mm	F	3.5±0.05 mm		P_2	2.0±0.05 mm
D_0	1.5+0.1/-0 mm	 G	0.75 mm (min.)	_	Т	0.25±0.03 mm
D ₁	0.5±0.1 mm	K ₀	0.62±0.05 mm		W	8.0+0.3/-0.1 mm
E ₁	1.75±0.1 mm	 Po	4.0±0.1 mm	_		

Table 1: Tape dimensions.



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11.2 Reel with diameter of 180 mm

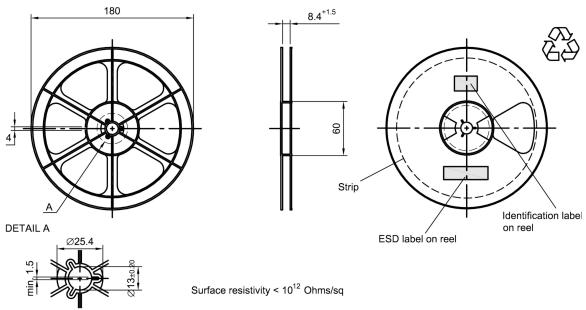


Figure 9: Drawing of reel (first-angle projection) with diameter of 180 mm.

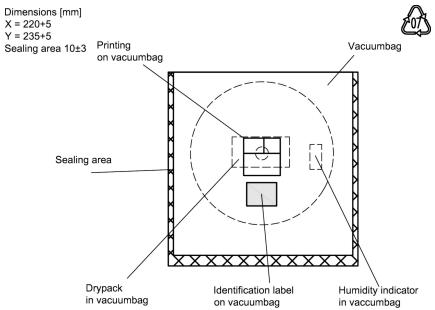


Figure 10: Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.



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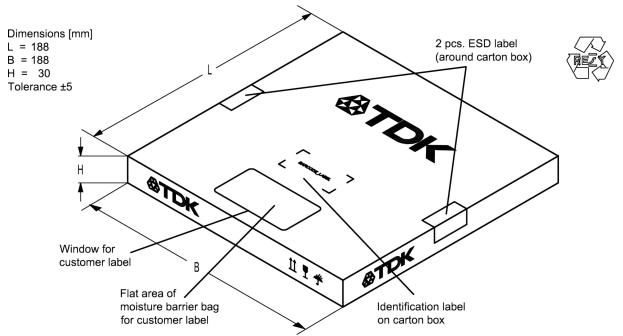


Figure 11: Drawing of folding box for reel with diameter of 180 mm.



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12 Marking

Products are marked with product type number and lot number encoded according to Table 2:

■ Type number:

The 4 digit type number of the ordering code, e.g., B3xxxxB1234xxxx, is encoded by a special BASE32 code into a 3 digit marking.

Example of decoding type number marking on device

in decimal code. 1234 => 1 x 32^2 + 6 x 32^1 + 18 (=J) x 32^0 1234 =

The BASE32 code for product type B4362 is 48A.

■ Lot number:

The last 5 digits of the lot number, 12345, eg., are encoded based on a special BASE47 code into a 3 digit marking.

Example of decoding lot number marking on device

in decimal code. 12345

5UY $5 \times 47^2 + 27 (=U) \times 47^1 + 31 (=Y) \times 47^0$

12345

Adopted BASE32 code for type number				
Decimal value	Base32 code	Decimal value	Base32 code	
0	0	16	G	
1	1	17	Н	
2	2	18	J	
3	3	19	K	
4	4	20	М	
5	5	21	N	
6	6	22	Р	
7	7	23	Q	
8	8	24	R	
9	9	25	S	
10	Α	26	Т	
11	В	27	V	
12	С	28	W	
13	D	29	X	
14	E	30	Υ	
15	F	31	Z	

Adopted BASE47 code for lot number				
Base47	Decimal	Base47		
code	value	code		
0	24	R		
1	25	S		
2	26	Т		
3	27	U		
4	28	V		
5	29	W		
6	30	X		
7	31	Υ		
8	32	Z		
9	33	b		
Α	34	d		
В 35		f		
C 36		h		
D	D 37 r			
E	38	r		
F	39	t		
G	40	v		
Н	41	\		
J	42	?		
K	43	{		
L	44	}		
М	45	<		
N	46	>		
Р				
	Base47 code 0 1 2 3 4 5 6 7 8 9 A B C D E F G H J K L M N	Base47 code Decimal value 0 24 1 25 2 26 3 27 4 28 5 29 6 30 7 31 8 32 9 33 A 34 B 35 C 36 D 37 E 38 F 39 G 40 H 41 J 42 K 43 L 44 M 45 N 46		

Table 2: Lists for encoding and decoding of marking.



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13 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3rd edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
T > 220 °C	30 s to 70 s
T > 230 °C	min. 10 s
T > 245 °C	max. 20 s
<i>T</i> ≥ 255 °C	-
peak temperature T_{peak}	250 °C +0/-5 °C
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

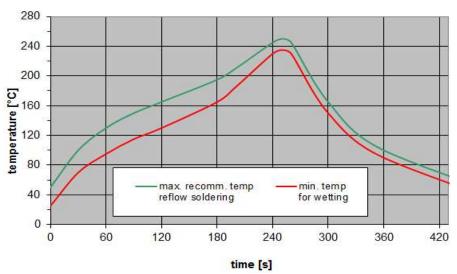


Figure 12: Recommended reflow profile for convection and infrared soldering – lead-free solder.



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14 ESD protection of SAW filters

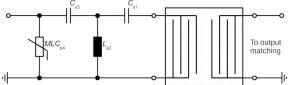
SAW filters are Electro Static Discharge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, "ESD matching" has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended "ESD matching" topologies.

For wide band filters the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.





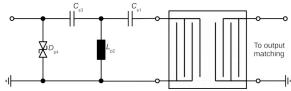


Figure 14: Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.

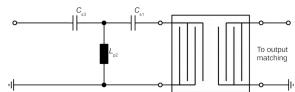


Figure 15: 3rd order high-pass structure for basic ESD protection.

In all three figures the shunt inductor L_{p2} could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to EPCOS Application report: "**ESD protection for SAW filters**". This report can be found under www.epcos.com/rke. Click on "Applications Notes".



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15 Annotations

15.1 Matching coils

See TDK inductor pdf-catalog http://www.tdk.co.jp/tefe02/coil.htm#aname1 and Data Library for circuit simulation http://www.tdk.co.jp/etvcl/index.htm.

15.2 Power Transfer Function (PTF) of WCDMA signal

Attenuation of WCDMA signal, $\alpha_{_{\text{WCDMA}}},$ is defined by

$$\alpha_{\text{WCDMA}}(f_{\text{carrier}}) = 10 \log_{10} \left| \frac{1}{\text{PTF}(f_{\text{carrier}})} \right| dB$$

and

$$\mathrm{PTF}(f_{\mathrm{carrier}}) = \int_{-\infty}^{+\infty} \left| S_{21}(f) H_{\mathrm{RRC}}(f - f_{\mathrm{carrier}}) \right|^2 \mathrm{d}f$$

with f_{carrier} according to 3GPP TS 25.101 (e.g., for the WCDMA B8 pass band, f_{carrier} ranges from 882.4 MHz to 912.6 MHz which correspond to the lowest and highest TX channels, respectively). $H_{\text{RRC}}(f)$ is the transfer function of the root-raised cosine transmit pulse shaping filter according to 3GPP TS 25.101 using the normalization

$$\int_{-\infty}^{+\infty} \left| H_{RRC}(f) \right|^2 \mathrm{d}f = 1 \quad .$$

15.3 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

15.4 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local EPCOS sales office.



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16 Cautions and warnings

16.1 Display of ordering codes for EPCOS products

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16.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

16.3 Moldability

Before using in overmolding environment, please contact your local EPCOS sales office.

16.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on EPCOS internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of EPCOS, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

Projection method

Unless otherwise specified first-angle projection is applied.



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