

# Gallium Nitride 28V, 200W RF Power Transistor

Built using the SIGANTIC® NRF1 process - A proprietary GaN-on-Silicon technology

#### **FEATURES**

- Optimized for narrowband and broadband applications from from DC – 1200MHz
- 200W P<sub>3dB</sub> CW power at 900MHz in quadrature combined or push-pull configuration
- 90W CW power from 500-1000MHz in application design AD-014
- · High efficiency from 14V to 28V
- 1.0 °C/W R<sub>TH</sub> with maximum T<sub>J</sub> rating of 200°C
- Robust up to 10:1 VSWR mismatch at all angles with no device degradation
- Subject to EAR99 export control



DC - 1200 MHz 14 - 28 Volt GaN HEMT



**RF Specifications (CW):**  $V_{DS} = 28V$ ,  $I_{DQ} = 1400 \text{mA}^1$ , Frequency = 900MHz,  $T_A = 25^{\circ}\text{C}$ , Measured in Nitronex Quadrature Combined Test Fixture<sup>2</sup>.

Symbol	Parameter	Min	Тур	Max	Units
P <sub>3dB</sub>	Average Output Power at 3dB Gain Compression	52.0	53.0	-	dBm
G <sub>SS</sub>	Small Signal Gain	17.3	18.3	-	dB
η	Drain Efficiency at 3dB Gain Compression <sup>2</sup>	57	63	-	%
VSWR	10:1 VSWR at all phase angles	No change in device performance			

Note 1: 700mA per transistor. Each gate should be biased independently to set desired I<sub>DQ</sub>.

Note 2: Includes ~ 0.2 dB quadrature combiner loss.

**Typical 2-Tone Performance:**  $V_{DS}$  = 28V,  $I_{DQ}$  = 1400mA<sup>1</sup>, Frequency = 900MHz, Tone spacing = 1MHz,  $T_A$  = 25°C Measured in Nitronex Quadrature Combined Test Fixture<sup>2</sup>.

Symbol	Parameter	Тур	Units
P <sub>3dB,PEP</sub>	Peak Envelope Power at 3dB Gain Compression	53.4	dBm
P <sub>1dB,PEP</sub>	Peak Envelope Power at 1dB Gain Compression	52.6	dBm
P <sub>IMD3</sub>	Peak Envelope Power at -35dBc IMD3	50.8	dBm

Note 1: 700mA per transistor. Each gate should be biased independently to set desired I<sub>DO</sub>.

Note 2: Includes ~ 0.2 dB quadrature combiner loss.

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# **DC Specifications:** Per Transistor, $T_A = 25^{\circ}C$

Symbol	Parameter	Min	Тур	Max	Units
Off Characteristics					
V <sub>BDS</sub>	Drain-Source Breakdown Voltage (V <sub>GS</sub> = -8V, I <sub>D</sub> = 36mA)	100	-	-	V
I <sub>DLK</sub>	Drain-Source Leakage Current (V <sub>GS</sub> = -8V, V <sub>DS</sub> = 60V)	-	9	18	mA
On Characteristics					
V <sub>T</sub>	Gate Threshold Voltage (V <sub>DS</sub> = 28V, I <sub>D</sub> = 36mA)	-2.3	-1.8	-1.3	V
$V_{GSQ}$	Gate Quiescent Voltage (V <sub>DS</sub> = 28V, I <sub>D</sub> = 700mA)	-2.0	-1.5	-1.0	V
R <sub>ON</sub>	On Resistance $(V_{GS} = 2V, I_D = 270mA)$	-	0.13	0.14	Ω
I <sub>D,MAX</sub>	Drain Current (V <sub>DS</sub> = 7V pulsed, 300μs pulse width, 0.2% duty cycle)	19.0	20.5	-	А

## **Absolute Maximum Ratings:** Not Simultaneous, Per Transistor, $T_A = 25$ °C Unless Otherwise Noted

Symbol	Parameter	Max	Units	
V <sub>DS</sub>	Drain-Source Voltage	100	V	
$V_{GS}$	Gate-Source Voltage	-10 to 3	V	
I <sub>G</sub>	Gate Current	180	mA	
P <sub>T</sub>	Total Device Power Dissipation (Derated above 25°C), both transistors on	175	W	
0	Thermal Resistance (Junction-to-Case), composite for both transistors on, T <sub>J</sub> = 180°C	1.0	°C/W	
$\theta$ JC	Thermal Resistance (Junction-to-Case), one transistor on, one off, $T_J = 180$ °C	1.8		
T <sub>STG</sub>	Storage Temperature Range	-65 to 150	°C	
$T_J$	Operating Junction Temperature	200	°C	
НВМ	Human Body Model ESD Rating (per JESD22-A114)	1C (>1000V)		
MM	Machine Model ESD Rating (per JESD22-A115)	A (>100V)		
CDM	Charge Device Model ESD Rating (per JESD22-C101)	IV (>4000V)		

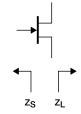


### Load-Pull Data, Reference Plane at Device Leads

 $V_{DS}$ =28V,  $I_{DQ}$ =700mA, One Single-Ended Transistor,  $T_A$ =25°C Unless Otherwise Noted

Table 1: Optimum Source and Load Impedances for CW Gain, Drain Efficiency, and Output Power Performance

Frequency (MHz)	<b>Z</b> <sub>S</sub> (Ω)	<b>Z</b> <sub>L</sub> (Ω)	P <sub>SAT</sub> (dBm)	G <sub>SS</sub> (dB)	Drain Efficiency @ P <sub>SAT</sub> (%)
500	1.4 + j0.1	2.0 + j0.5	50.0	24.0	70%
900	1.6 - j1.5	2.3 - j1.5	50.0	18.5	74%
1200	1.8 - j2.7	3.5 - j2.8	49.5	16.5	62%



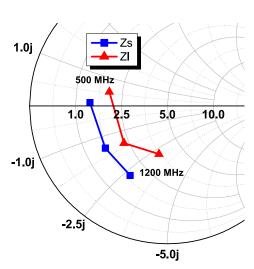


Figure 1 - Optimum Impedances for CW Performance

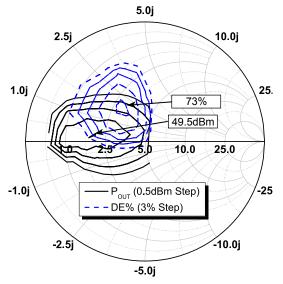


Figure 2 - Load-Pull Contours, 500MHz,  $P_{IN}$  = 25dBm,  $Z_{S}$  = 1.4 + j0.1  $\Omega$ 

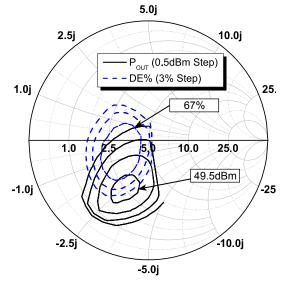


Figure 3 - Load-Pull Contours, 900MHz,  $P_{IN}$  = 30dBm,  $Z_{S}$  = 1.6 - j1.5  $\Omega$ 

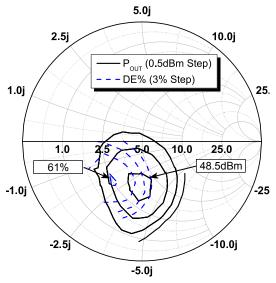
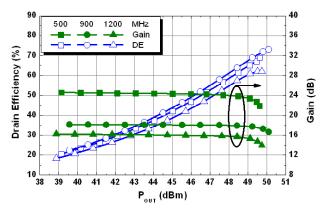


Figure 4 - Load-Pull Contours, 1200MHz,  $P_{IN}$  = 32dBm,  $Z_{S}$  = 1.8 - j2.7  $\Omega$ 



### Load-Pull Data per Device Lead, Reference Plane at Device Leads

 $V_{DS}$ =28V,  $I_{DQ}$ =700mA, One Single-Ended Transistor,  $T_{A}$ =25°C unless otherwise noted.



**Figure 5 -** Typical CW Performance, over Frequency

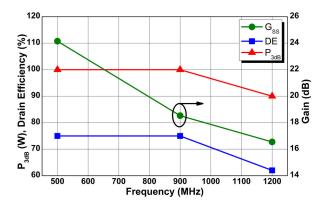
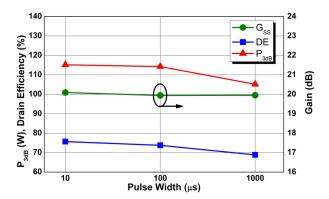


Figure 6 - Typical CW Performance over Frequency



**Figure 7 -** Typical Pulsed Performance, Frequency = 900MHz, Duty Cycle = 10%

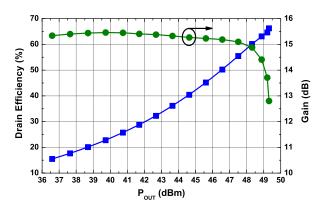
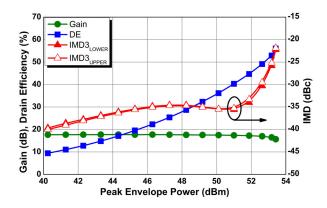


Figure 8 - Typical CW Performance at  $V_{DS}$  = 20V Frequency = 900MHz

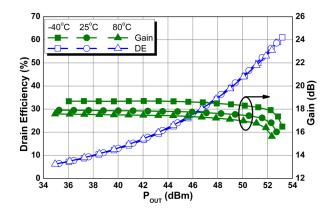


#### **Nitronex Quadrature Combined Test Fixture**

 $V_{DS}$ =28V,  $I_{DO}$ =1400mA,  $T_A$ =25°C unless otherwise noted.



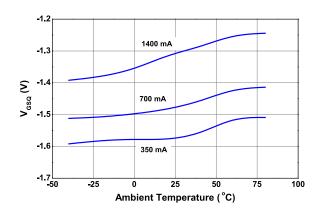
**Figure 9 -** Typical IMD3 Performance, Frequency = 900MHz, Tone spacing = 1MHz



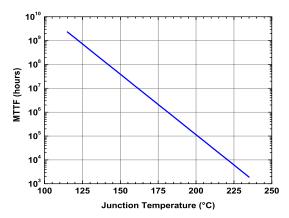
**Figure 10 -** Typical CW Performance over Temperature, Frequency = 900MHz

### **Typical Device Characteristics**

V<sub>DS</sub>=28V, I<sub>DQ</sub>=700mA, One Single-Ended Transistor, T<sub>A</sub>=25°C unless otherwise noted.



**Figure 11 -** Quiescient Gate Voltage ( $V_{GSQ}$ ) Required to Reach  $I_{DQ}$  over Temperature



**Figure 12 -** MTTF of NRF1 devices as a function of junction temperature



#### Ordering Information<sup>1</sup>

Part Number	Description
NPT1007B	NPT1007 in AC780B-4 Metal-Ceramic Bolt-Down Package

<sup>1:</sup> To find a Nitronex contact in your area, visit our website at http://www.nitronex.com

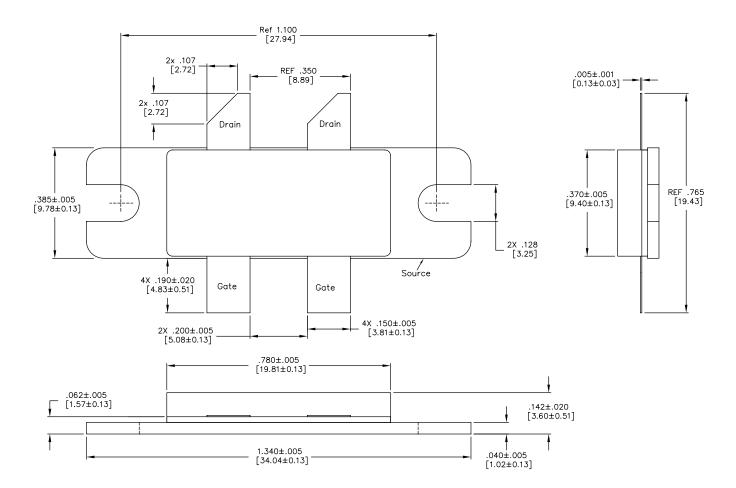


Figure 13 - AC780B-4 Metal-Ceramic Package Dimensions and Pinout (all dimensions are in inches [mm])

# **NPT1007**



Nitronex, LLC 2305 Presidential Drive Durham, NC 27703 USA +1.919.807.9100 (telephone) +1.919.807.9200 (fax) info@nitronex.com www.nitronex.com

#### **Additional Information**

This part is lead-free and is compliant with the RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

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