



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _A = +25°C
-30V	45mΩ @ V _{GS} = -10V	-4.3A
-30 <i>V</i>	65mΩ @ V _{GS} = -4.5V	-3.3A

Description

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- · General purpose interfacing switches
- Power management functions
- Analog switches

Features

- Low Gate Threshold Voltage
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

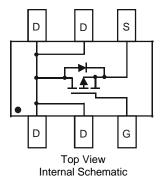
https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOT26
- Package Material Molded Plastic, "Green" Molding Compound.
 UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ©3
- Terminal Connections: See Diagram
- Weight: 0.016 grams (Approximate)



Top View



Ordering Information (Note 4)

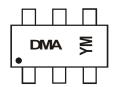
Part Number	Package	Packing		
Fart Number	Package	Qty.	Carrier	
DMP3056LDM-7	SOT26	3000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



DMA = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022)M = Month (ex: 8 = August)

Date Code Key

Year	2008		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	V		J	K	L	М	N	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Doo
WOILLI	Jan	1 60	iviai	Aþi	iviay	Juli	Jui	Aug	Sep	UCI	NOV	Dec

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		VDSS	-30	V	
Gate-Source Voltage		Vgss	±20	V	
Continuous Proin Current (Note 5) V 10V	Steady State	T _A = +25°C	ID	-4.3	Α
Continuous Drain Current (Note 5) V _{GS} = -10V	t < 10s	$T_A = +25$ °C	I _D	-5.8	А
Maximum Continuous Body Diode Forward Curr	ent (Note 5)	Is	-2.3	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle =	1%)	I _{DM}	-13	A	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	1.25	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	100	°C/W
Total Power Dissipation (Note 5)	T _A = +25°C	PD	1.5	W
Thermal Resistance, Junction to Ambient (Note 5) Steady State		RθJA	86	°C/W
Thermal Resistance, Junction to Case		Rejc	15.6	*C/VV
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Notes:

5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad.6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



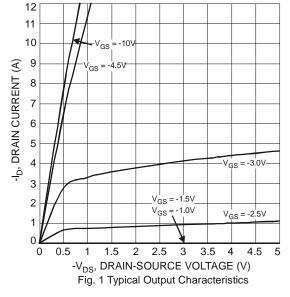
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

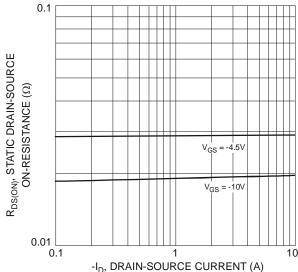
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
STATIC PARAMETERS (Note 7)				•	•		
Drain-Source Breakdown Voltage	BVDSS	-30	_	_	V	Vgs = 0V, ID = -250µA	
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	_	-1	μA	Vgs = 0V, Vps = -30V	
Gate-Body Leakage Current	Igss			±100 ±800	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$ $V_{GS} = \pm 25V, V_{DS} = 0V$	
Gate Threshold Voltage	Vgs(TH)	-1.0	_	-2.1	V	Vgs = Vps, Ip = -250µA	
Static Drain-Source On-Resistance	RDS(ON)			45 65	mΩ	$V_{GS} = -10V, I_{D} = -5A$ $V_{GS} = -4.5V, I_{D} = -4.2A$	
Forward Transconductance	grs	_	8	_	s	$V_{DS} = -10V$, $I_{D} = -4.3A$	
Diode Forward Voltage	V_{SD}	-	_	-1.2	V	$V_{GS} = 0V$, $I_{S} = -1.7A$	
DYNAMIC PARAMETERS (Note 8)							
Input Capacitance		1	948	_	pF	., .,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
Output Capacitance		_	105	_	pF	V _G S = 0V, V _D S = -25V f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	100	_	pF	1 = 1.01/11/12	
SWITCHING CHARACTERISTICS (Note 8)							
Total Gate Charge	Qg		10.1	_	nC	$V_{DS} = -15V$, $V_{GS} = -4.5V$ $I_{D} = -6A$	
	Q_g	-	21.1	_		45)/)/ 40)/	
Gate-Source Charge	Qgs	-	2.8	_	nC	$V_{DS} = -15V, V_{GS} = -10V$ $I_{D} = -6A$	
Gate-Drain Charge	Q_{gd}	1	3.2	_		10 = -0A	
Gate Resistance	R_g	_	13.15	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Turn-On Delay Time		_	10.2	_			
Rise Time	t _R		6.6	_		V _{DS} = -15V, V _{GS} = -10V	
Turn-Off Delay Time	tD(OFF)	_	50.1	_	ns	$I_D = -1A$, $R_g = 6.0\Omega$	
Fall Time			22.3	_			

Notes:

^{7.} Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.









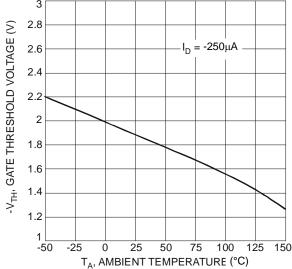
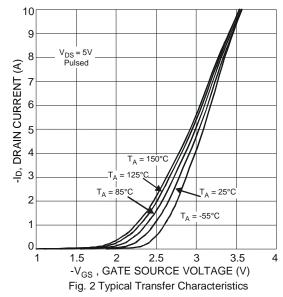


Fig. 5 Gate Threshold Variation vs. Ambient Temperature



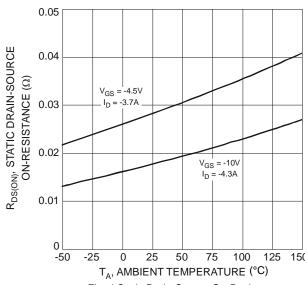


Fig. 4 Static Drain-Source On-Resistance vs. Ambient Temperature

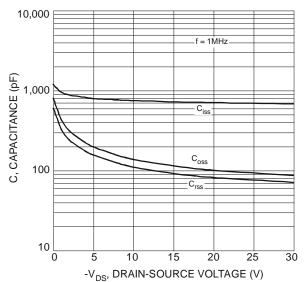
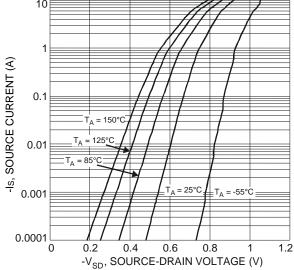
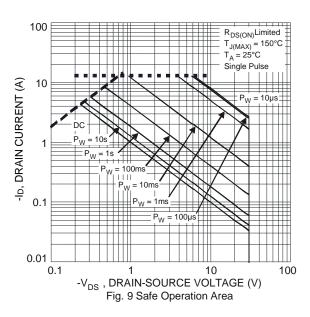


Fig. 6 Typical Total Capacitance









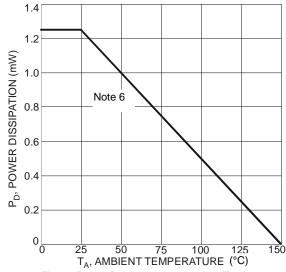


Fig. 11 Power Dissipation vs. Ambient Temperature

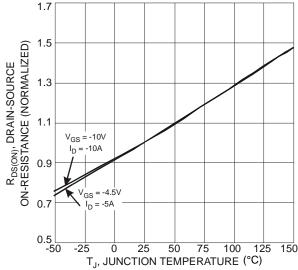
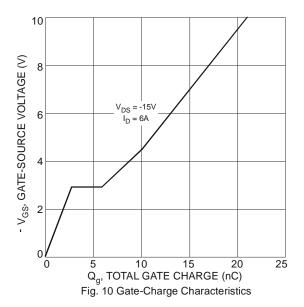


Fig. 8 On-Resistance Variation with Temperature





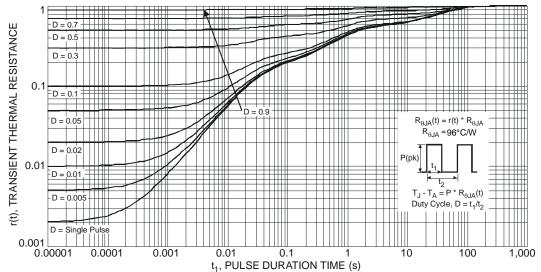


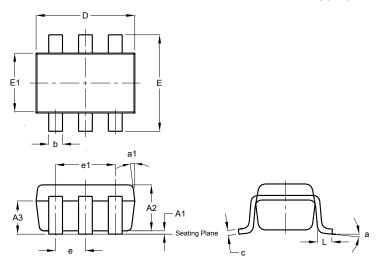
Fig. 12 Transient Thermal Response



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26

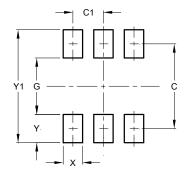


	SOT26						
Dim	Min	Max	Тур				
A1	0.013	0.10	0.05				
A2	1.00	1.30	1.10				
A3	0.70	0.80	0.75				
b	0.35	0.50	0.38				
С	0.10	0.20	0.15				
D	2.90	3.10	3.00				
е	-	-	0.95				
e1	-	-	1.90				
Е	2.70	3.00	2.80				
E1	1.50	1.70	1.60				
L	0.35	0.55	0.40				
а	-	-	8°				
a1	-	-	7°				
All	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Χ	0.55
Υ	0.80
Y1	3.20



IMPORTANT NOTICE

- 1. DIODES INCORPORATED (Diodes) AND ITS SUBSIDIARIES MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes' products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes' products. Diodes' products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of Diodes' products for their intended applications, (c) ensuring their applications, which incorporate Diodes' products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes' products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes' products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes' products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.
- 9. This Notice may be periodically updated with the most recent version available at https://www.diodes.com/about/company/terms-and-conditions/important-notice

DIODES is a trademark of Diodes Incorporated in the United States and other countries. The Diodes logo is a registered trademark of Diodes Incorporated in the United States and other countries. © 2022 Diodes Incorporated. All Rights Reserved.

www.diodes.com