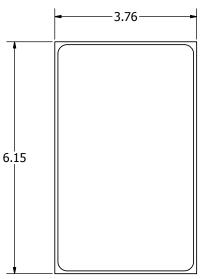


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REVISION HISTORY									
LTR	DESCRIPTION	DATE	APPROVED						
-	RELEASED FROM ENGINEERING	2/14/2008							

INTERFACING CIRCUIT O POWER (NOTE: CUSTOMER DEFINED) POWER OUTPUT MEMS MIC



<del>--</del> 1.25 <del>--</del>

2.16 -0.330.94 POWER-0.38 0.63 -OUTPUT -GROUND -GROUND 1.17 Ø0.76 SOUND HOLE Ø1.02 1.62 1.24 0.76 -0.44 -1.88-



## NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. SPECIFICATIONS SUBJECT TO CHANGE OR WITHDRAWL WITHOUT NOTICE.
- 3. THIS PART IS RoHS 2002/95/EC COMPLIANT.
- 4. MAXIMUM OF 120dB INPUT SPL (FOR LESS THAN 0.5% DISTORTION).
- 5. OUTPUT SIGNAL @ 94dB SPL, 1KHz (-45dB/5.65mV,-40dB/10.04mV).
- 6. 1dB SENSITIVITY VARIATION OVER OPERATING RANGE (1.5-3.6V).

UNLESS OTHERWISE SPECIFIED:	0175	
DIMENSIONS ARE IN MILLIMETERS,	SIZE	
TOLERANCES ARE ±0.5 AND ANGLES ARE ±3°.	S AND HO	
POSM-1342RM-C3310-R.idw		

PILI		P	POSM-1342RM-C3310-R				
J.D.	2/14/2008	R.W.	2/14/2008	B.R.	2/14/2008	2/14/2008	
Designed by	Date	Checked by	Date	Approved by	Date	Drawn Date	

Edition Microphone 1/1