Vishay Siliconix

N-Channel 100 V (D-S) MOSFET



Marking code: G2

PRODUCT SUMMARY					
V _{DS} (V)	100				
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10 \text{ V}$	0.149				
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 4.5 \text{ V}$	0.180				
Q _g typ. (nC)	2.2				
I _D (A) ^a	2.3				
Configuration	Single				

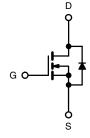
FEATURES

- TrenchFET® Gen IV power MOSFET
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912



APPLICATIONS

- DC/DC converters / boost converters
- · Load switch
- LED backlighting in LCD TVs
- · Power management for mobile computing



N-Channel MOSFET

ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and halogen-free	Si2392BDS-T1-GE3

ABSOLUTE MAXIMUM RATINGS (TA	= 25 °C, unless other	wise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage		V_{DS}	100	V	
Gate-source voltage	V_{GS}	± 20			
Continuous drain current (T _J = 150 °C)	T _C = 25 °C		2.3		
	T _C = 70 °C		1.8		
	T _A = 25 °C	I _D	2.0 b, c		
	T _A = 70 °C		1.6 ^{b, c}		
Pulsed drain current (t = 300 µs)		I _{DM}	6	Α	
Continuous source-drain diode current	T _C = 25 °C	,	1.4		
	T _A = 25 °C	I _S	1 b, c	٦	
Single pulse avalanche current	. 0.111	I _{AS}	4		
Single pulse avalanche energy	L = 0.1 mH	E _{AS}	0.8	mJ	
Maximum power dissipation	T _C = 25 °C		1.7		
	T _C = 70 °C	Б	1.1	w	
	T _A = 25 °C	P _D	1.25 ^{b, c}	VV	
	T _A = 70 °C		0.8 b, c		
Operating junction and storage temperature range	T _J , T _{stg}	-55 to +150	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient b, d	t ≤ 5 s	R_{thJA}	75	100	°C/W	
Maximum junction-to-foot (drain)	Steady state	R_{thJF}	40	75	C/VV	

Notes

- a. Based on T_C = 25 °C
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 5 s
- d. Maximum under steady state conditions is 166 °C/W



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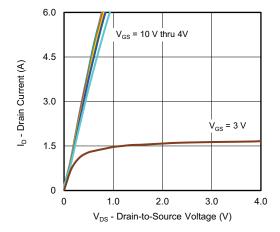
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static					•	•	
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	I _D =10 mA	-	80	-		
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA	-	-5	-	mV/°C	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.2	-	3	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
		V _{DS} = 100 V, V _{GS} = 0 V	-	-	1	T .	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10	μA	
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	3	-	-	Α	
Drain-source on-state resistance ^a		$V_{GS} = 10 \text{ V}, I_D = 2 \text{ A}$	-	0.124	0.149	 	
	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 1 A	-	0.138	0.180	Ω	
Forward transconductance a	9 _{fs}	$V_{DS} = 20 \text{ V}, I_D = 2 \text{ A}$	-	12	-	S	
Dynamic ^b				•	•		
Input capacitance	C _{iss}		-	290	-	pF	
Output capacitance	C _{oss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	26	-		
Reverse transfer capacitance	C _{rss}		-	5	-		
Total coloraba as a		$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$	-	4.7	7.1	nC	
Total gate charge	Q _g		-	2.2	3.3		
Gate-source charge	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 0.5 \text{ A}$	-	1	-		
Gate-drain charge	Q _{qd}		-	0.5	-		
Gate resistance	R_g	f = 1 MHz	0.3	1.5	3	Ω	
Turn-on delay time	t _{d(on)}		-	15	30		
Rise time	t _r	$V_{DD} = 50 \text{ V}, R_L = 31.25 \Omega$	-	15	30	1	
Turn-off delay time	t _{d(off)}	$I_D = 1.6 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$	-	13	26	1	
Fall time	t _f		-	8	16	1	
Turn-on delay time	t _{d(on)}		-	8	16	ns	
Rise time	t _r	$V_{DD} = 50 \text{ V}, R_L = 31.25 \Omega$	-	5	10	-	
Turn-off delay time	t _{d(off)}	$I_D = 1.6 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	15	30		
Fall time	t _f		-	5	10		
Drain-Source Body Diode Characteristi	cs				•	•	
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	1.4	^	
Pulse diode forward current ^a	I _{SM}		-	-	6	A	
Body diode voltage	V_{SD}	I _S = 1.6 A	-	0.8	1.2	V	
Body diode reverse recovery time	t _{rr}		-	21	32	ns	
Body diode reverse recovery charge	Q _{rr}	$I_F = 1.6 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s},$	-	21	32	nC	
Reverse recovery fall time	t _a	$T_J = 25 ^{\circ}C$	-	19	-		
Reverse recovery rise time	t _b		-	2	-	ns	

Notes

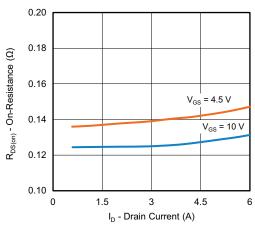
- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

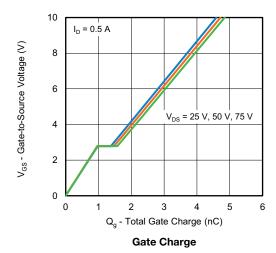


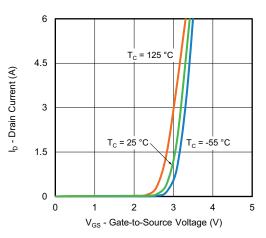


Output Characteristics

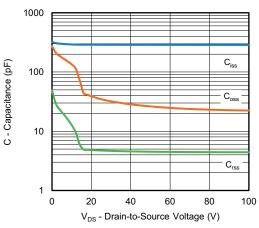


On-Resistance vs. Drain Current

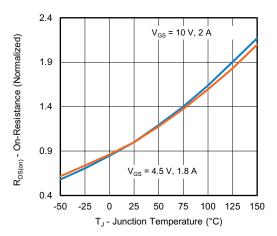




Transfer Characteristics

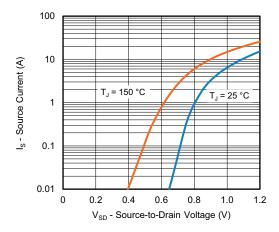


Capacitance

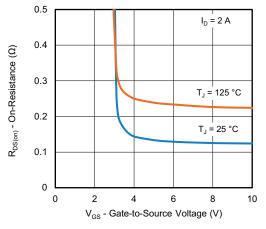


On-Resistance vs. Junction Temperature

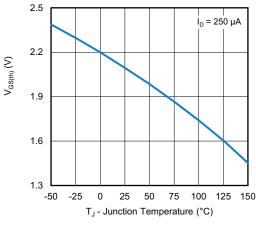




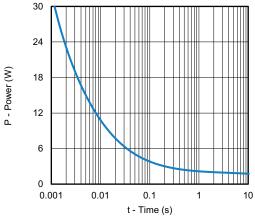
Source-Drain Diode Forward Voltage



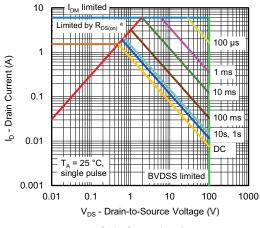
On-Resistance vs. Gate-to-Source Voltage



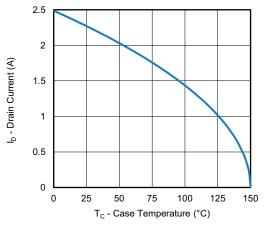
Threshold Voltage



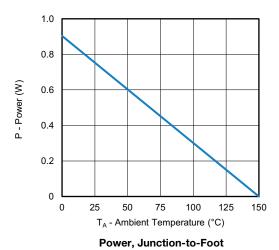
Single Pulse Power

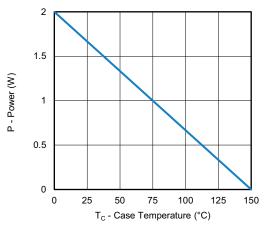






Current Derating a



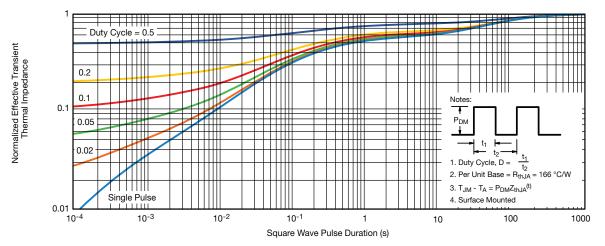


Power, Junction-to-Ambient

Note

a. The power dissipation P_D is based on T_J max.= 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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