RENESAS

UPD70F3347GJ(A)-UEN-A

The V850ES/SG3 and V850ES/SJ3 are 32-bit single-chip microcontrollers that includes the V850ES CPU core and peripheral functions such as ROM/RAM, a timer/counter, serial interfaces, an A/D converter, and a D/A converter. The V850ES/SG3 and V850ES/SJ3 includes an IEBus ® (Inter Equipment Bus ®) controller for the automotive LAN, and some of the models also include a CAN (Controller Area Network) controller.

EOL Announced Products

EOL Announced Products:

Discontinuation of production has been announced for these products. They should no longer be used in new designs.

PARAMETRICS

Parameters	UPD70F3347GJ(A)-UEN-A
Memory	
Program memory	768 KB Flash memory
RAM	60 KB
CPU (Bit, Clock, DMA, External Bus etc)	
CPU	V850ES (32-bit)
Max. Frequency	32 MHz
Sub-Clock (32.768 kHz)	YES
On-chip Oscillator Freq.	Low Speed 220 kHz
PLL	YES
Low Voltage Detection	YES
DMA Remarks	DMAC x 4 ch
External Address/Data Bus	YES
External Interrupt Pins	10
I/O Ports	128
Timers	
16-bit Timers	11 ch
Watchdog Timers	1 ch
Other Timers	Watch Timer x 1 ch
PWM Output	12
Analog	
A/D Converters	10-bit x 16 ch
D/A Converters	8-bit x 2 ch
Interfaces	
CSIs	6 ch
UARTS	4 ch
I2Cs	3 ch
LIN	4 ch
IEBus	1 ch
Operating Conditions	
Operating Voltage	2.85 to 3.6 V
Operating Voltage Operating Temperature	2.85 to 3.6 V -40 to 85 °C (Ambient Temperature)
Operating Temperature	

https://www.renesas.com/us/en/products/microcontrollers-microprocessors/v850/v850essx/v850essg3-sj3/device/UPD70F3347GJ(A)-UEN-A.html#overviewInfo/1-31-19