COMPLIANT

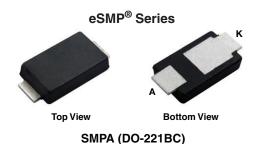
HALOGEN

FREE



Vishay General Semiconductor

Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier





LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS			
I _{F(AV)}	8.0 A		
V_{RRM}	50 V		
I _{FSM}	120 A		
V_F at $I_F = 8.0 \text{ A} (T_A = 125 ^{\circ}\text{C})$	0.41 V		
T _J max.	150 °C		
Package	SMPA (DO-221BC)		
Circuit configuration	Single		

FEATURES

- Very low profile typical height of 0.95 mm
- · Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMPA (DO-221BC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD22-B102

M3 suffix meets JESD 201 class 2 whisker test **Polarity:** Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V8PAN50	UNIT	
Device marking code		8N5		
Maximum repetitive peak reverse voltage	V _{RRM}	50	V	
Maximum DC forward current	I _F ⁽¹⁾	8.0	^	
	I _F ⁽²⁾	3.7	— A	
Maximum DC reverse voltage	V _{DC}	35	V	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	120	А	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Notes

- (1) Units mounted on 3 cm x 3 cm Aluminum, 2 oz. PCB
- (2) Free air, mounted on recommended copper pad area



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST C	TEST CONDITIONS		TYP.	MAX.	UNIT	
Instantaneous forward voltage	I _F = 4.0 A	T _A = 25 °C	V _F ⁽¹⁾	0.42	-	V	
	$I_F = 8.0 A$			0.48	0.56		
	$I_F = 4.0 A$	T _A = 125 °C	'	VF \''	0.32	-	V
	$I_F = 8.0 A$			0.41	0.50		
Reverse current	V _R = 35 V	T _A = 25 °C		25	-	μΑ	
	V _R = 35 V	T _A = 125 °C	I _R ⁽²⁾	19	-	mA	
	V _R = 50 V	T _A = 25 °C	IR (−/	-	1500	μA	
	v _R = 50 v	T _A = 125 °C		31	70	mA	
Typical junction capacitance	4.0 V, 1 MI	4.0 V, 1 MHz		1060	-	pF	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise specified)				
PARAMETER	SYMBOL V8PAN50			
Typical thermal resistance	R _{0JA} (1)	100	°C/W	
	R _{0JM} (2)	5	C/VV	

Notes

- (1) Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance R_{θJA} junction to ambient
- Units mounted on 3 cm x 3 cm Aluminum, 2 oz. pad area; thermal resistance $R_{\theta JM}$ junction to mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V8PAN50-M3/I	0.032	I	14 000	13" diameter plastic tape and reel		

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise specified)

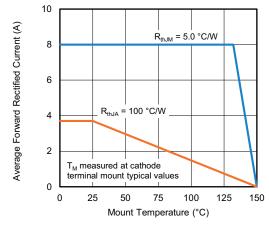


Fig. 1 - Maximum Forward Current Derating Curve

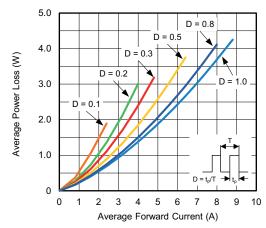


Fig. 2 - Forward Power Loss Characteristics



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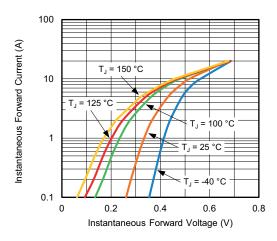


Fig. 3 - Typical Instantaneous Forward Characteristics

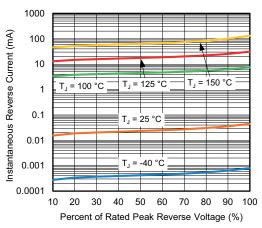


Fig. 4 - Typical Reverse Leakage Characteristics

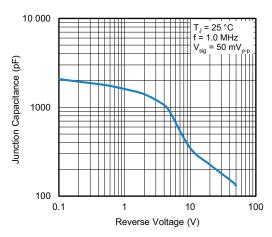


Fig. 5 - Typical Junction Capacitance

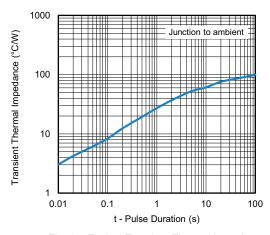


Fig. 6 - Typical Transient Thermal Impedance

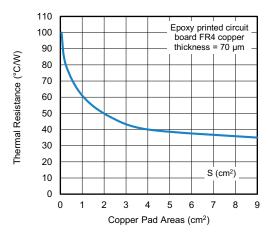


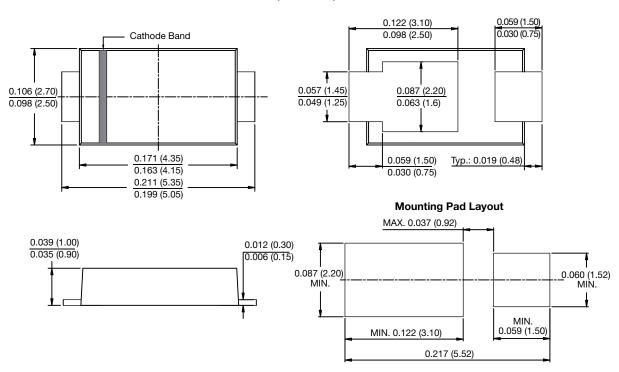
Fig. 7 - Thermal Resistance Junction to Ambient vs. Copper Pad Areas



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

SMPA (DO-221BC)





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