



## QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS QUICKSWITCH 24:12 MUX/DEMUX

IDTQS33X257

### FEATURES:

- Enhanced N channel FET with no inherent diode to Vcc
- Bidirectional signal flow
- 24:12 Mux/Demux switches connect inputs to outputs
- Individual controls for each bank
- Zero propagation delay, zero ground bounce
- Undershoot clamp diodes on all switch and control inputs
- TTL-compatible control inputs
- Available in 48-pin QVSOP package

### APPLICATIONS:

- Logic replacement
- Video, audio, graphics switching, muxing
- Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)
- Bus funneling

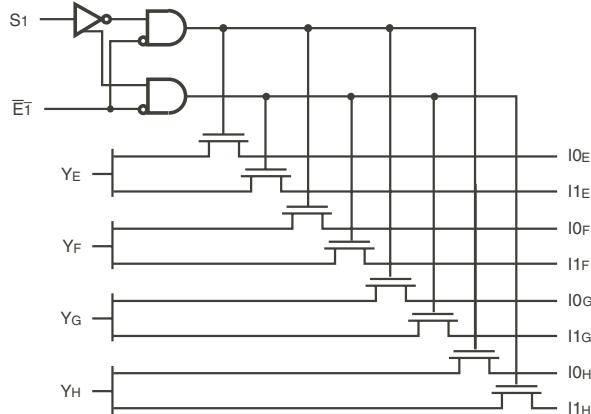
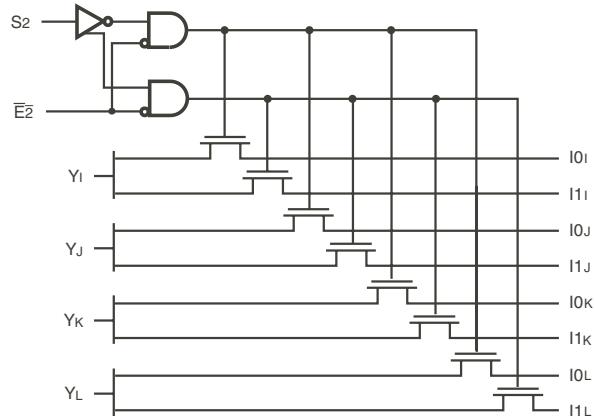
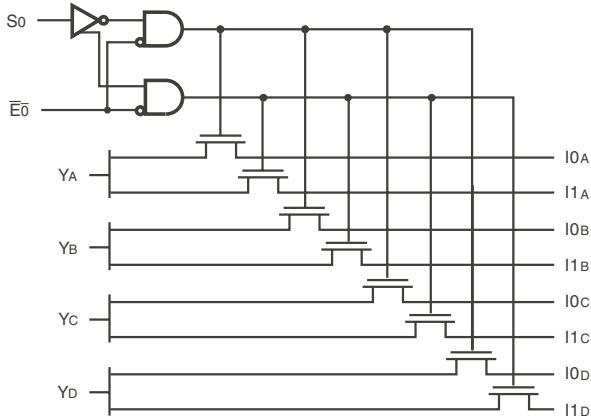
### DESCRIPTION:

The QS33X257 is a high-speed CMOS TTL-compatible 24:12 multiplexer/demultiplexer. The QS33X257 is functionally compatible to three of the QuickSwitch version of the 74F257, 74FCT257, and the 74ALS/AS/LS257 Quad 2:1 multiplexers. The low ON resistance of the QS33X257 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise. This part will be used in wide bus multiplexing where board space is at a premium.

Mux/Demux devices provide an order of magnitude faster speed than equivalent logic devices.

The QS33X257 is characterized for operation at -40°C to +85°C.

### FUNCTIONAL BLOCK DIAGRAM



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**INDUSTRIAL TEMPERATURE RANGE**

**AUGUST 2012**

## PIN CONFIGURATION

S0	1	48	VCC
I0A	2	47	$\bar{E}0$
I1A	3	46	I0D
YA	4	45	I1D
I0B	5	44	YD
I1B	6	43	I0C
YB	7	42	I1C
GND	8	41	YC
S1	9	40	VCC
I0E	10	39	$\bar{E}1$
I1E	11	38	I0H
YE	12	37	I1H
I0F	13	36	YH
I1F	14	35	I0G
YF	15	34	I1G
GND	16	33	YG
S2	17	32	VCC
I0I	18	31	$\bar{E}2$
I1I	19	30	I0L
YI	20	29	I1L
I0J	21	28	YL
I1J	22	27	I0K
YJ	23	26	I1K
GND	24	25	YK

QVSOP  
TOP VIEW

## ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>

Symbol	Description	Max	Unit
VTERM <sup>(2)</sup>	Supply Voltage to Ground	-0.5 to +7	V
VTERM <sup>(3)</sup>	DC Switch Voltage Vs	-0.5 to +7	V
VTERM <sup>(3)</sup>	DC Input Voltage V <sub>IN</sub>	-0.5 to +7	V
VAC	AC Input Voltage (pulse width ≤ 20ns)	-3	V
I <sub>OUT</sub>	DC Output Current Max. Sink Current/Pin	120	mA
P <sub>MAX</sub>	Maximum Power Dissipation	0.5	W
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C

NOTE:

1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

2. Vcc terminals.

3. All terminals except Vcc.

## CAPACITANCE

(T<sub>A</sub> = +25°C, f = 1.0MHz, V<sub>IN</sub> = 0V, V<sub>OUT</sub> = 0V)

Pins		Typ.	Max. <sup>(1)</sup>	Unit
Control Pins		4	5	pF
Quickswitch Channels (Switch OFF)	Demux	5	7	pF
	Mux	9	10	

NOTE:

1. This parameter is measured at characterization but not tested.

## PIN DESCRIPTION

Pin Names	I/O	Description
I <sub>xx</sub>	I/O	Data Inputs
S <sub>x</sub>	I	Select Input
$\bar{E}x$	I/O	Enable Input
Y <sub>x</sub>	I/O	Data Outputs

## FUNCTION TABLE<sup>(1)</sup>

Enable		Outputs				Function
$\bar{E}x$	S <sub>x</sub>	Y <sub>A</sub>	Y <sub>B</sub>	Y <sub>C</sub>	Y <sub>D</sub>	
H	X	Z	Z	Z	Z	Disable
L	L	I <sub>0A</sub>	I <sub>0B</sub>	I <sub>0C</sub>	I <sub>0X</sub>	Select 0
L	H	I <sub>1A</sub>	I <sub>1B</sub>	I <sub>1C</sub>	I <sub>1X</sub>	Select 1

NOTE:

1. H = HIGH Voltage Level

L = LOW Voltage Level

X = Don't Care

Z = High-Impedance

## DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

Industrial: TA = -40°C to +85°C, Vcc = 5.0V ±5%

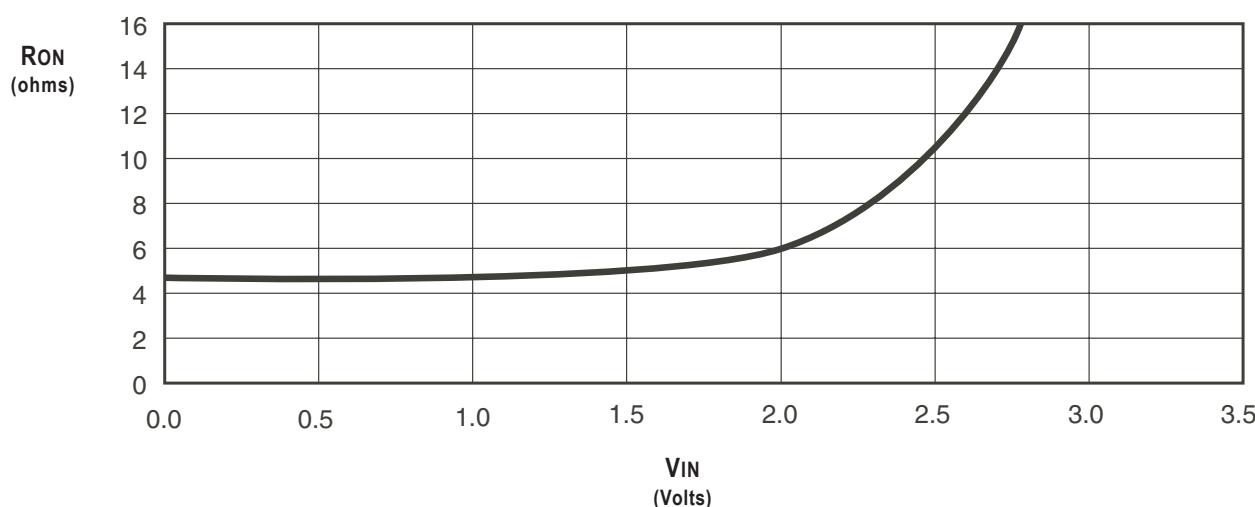
Symbol	Parameter	Test Conditions	Min.	Typ. <sup>(1)</sup>	Max.	Unit
V <sub>IH</sub>	Input HIGH Level	Guaranteed Logic HIGH for Control Pins	2	—	—	V
V <sub>IL</sub>	Input LOW Level	Guaranteed Logic LOW for Control Pins	—	—	0.8	V
I <sub>IN</sub>	Input Leakage Current (Control Inputs)	0V ≤ V <sub>IN</sub> ≤ V <sub>CC</sub>	—	—	±1	µA
I <sub>OZ</sub>	Off-State Output Current (Hi-Z)	0V ≤ V <sub>OUT</sub> ≤ V <sub>CC</sub>	—	—	±1	µA
R <sub>ON</sub>	Switch ON Resistance	V <sub>CC</sub> = Min., V <sub>IN</sub> = 0V, I <sub>ON</sub> = 30mA	—	5	7	Ω
		V <sub>CC</sub> = Min., V <sub>IN</sub> = 2.4V, I <sub>ON</sub> = 15mA	—	10	15	
V <sub>P</sub>	Pass Voltage <sup>(2)</sup>	V <sub>IN</sub> = V <sub>CC</sub> = 5V, I <sub>OUT</sub> = -5µA	3.7	4	4.2	V

NOTES:

1. Typical values are at V<sub>CC</sub> = 5.0V, TA = 25°C.

2. Pass Voltage is guaranteed but not production tested.

## TYPICAL ON RESISTANCE vs V<sub>IN</sub> AT V<sub>CC</sub> = 5V



## POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions <sup>(1)</sup>	Max.	Unit
I <sub>CCQ</sub>	Quiescent Power Supply Current	V <sub>CC</sub> = Max., V <sub>IN</sub> = GND or V <sub>CC</sub> , f = 0	9	µA
ΔI <sub>CC</sub>	Power Supply Current per Control Input HIGH <sup>(2)</sup>	V <sub>CC</sub> = Max., V <sub>IN</sub> = 3.4V, f = 0	1.5	mA
I <sub>CCD</sub>	Dynamic Power Supply Current per MHz <sup>(3)</sup>	V <sub>CC</sub> = Max., I and Y pins open Control Inputs Toggling at 50% Duty Cycle	0.25	mA/MHz

**NOTES:**

1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
2. Per TLL driven input (V<sub>IN</sub> = 3.4V, control inputs only). I and Y pins do not contribute to ΔI<sub>CC</sub>.
3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The I and Y inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

## SWITCHING CHARACTERISTICS OVER OPERATING RANGE

T<sub>A</sub> = -40°C to +85°C, V<sub>CC</sub> = 5.0V ± 5%;

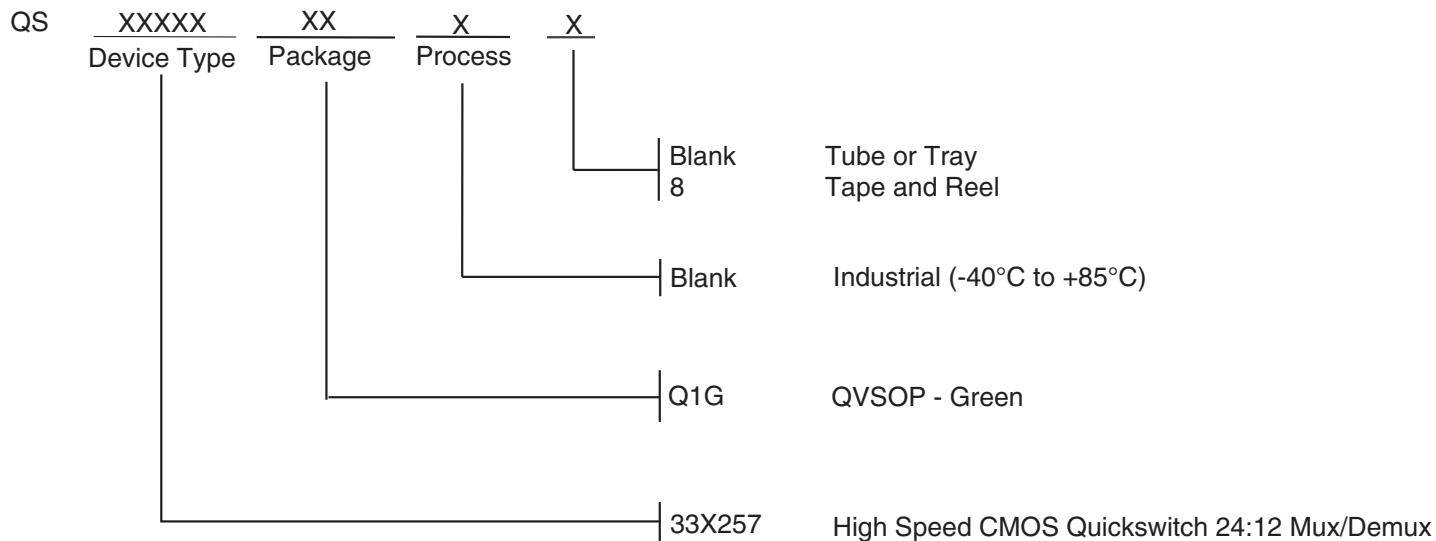
C<sub>LOAD</sub> = 50pF, R<sub>LOAD</sub> = 500Ω unless otherwise noted.

Symbol	Parameter	Min. <sup>(1)</sup>	Typ.	Max.	Unit
t <sub>PLH</sub>	Data Propagation Delay <sup>(2,3)</sup> Ix to Y	—	0.25	—	ns
t <sub>PHL</sub>	Switch Turn-on Delay Sx to Y	0.5	—	5.2	ns
t <sub>PZL</sub>	Switch Turn-off Delay Ex to Y	0.5	—	4.8	ns
t <sub>PZH</sub>	Switch Turn-off Delay <sup>(2)</sup> Ex to Y, Sx to Y	0.5	—	5	ns

**NOTES:**

1. Minimums are guaranteed but not production tested.
2. This parameter is guaranteed but not production tested.
3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for C<sub>L</sub> = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## ORDERING INFORMATION



## Datasheet Document History

10/01/08	Pg. 5	Updated the ordering information by removing the "IDT" notation.
08/13/12	Pg. 5	Updated the ordering information by removing non RoHS part and by adding Tape and Reel information.



**CORPORATE HEADQUARTERS**  
6024 Silver Creek Valley Road  
San Jose, CA 95138

*for SALES:*  
800-345-7015 or 408-284-8200  
fax: 408-284-2775  
[www.idt.com](http://www.idt.com)

*for Tech Support:*  
[logichelp@idt.com](mailto:logichelp@idt.com)