# NPN Power Silicon Transistor



#### **Features**

- Available in commercial, JAN, JANTX, JANTXV, JANS and JANSR 100K rads (Si) per MIL-PRF-19500/560
- TO-39 (TO-205AD) Package





## **Maximum Ratings**

Ratings	Symbol	Value	Units
Collector - Emitter Voltage	V <sub>CEO</sub>	100	Vdc
Collector - Base Voltage	V <sub>CBO</sub>	100	Vdc
Emitter - Base Voltage	V <sub>EBO</sub>	6.0	Vdc
Base Current	IB	1.0	Adc
Collector Current	lC	5.0	Adc
Total Power Dissipation @ $T_A = 25 ^{\circ}\text{C}$ @ $T_C = 25 ^{\circ}\text{C}$	P <sub>T</sub>	1.0 17.5	W
Operating & Storage Temperature Range	T <sub>op</sub> , T <sub>stg</sub>	-65 to +200	°C
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	10	°C/W

### **Electrical Characteristics**

OFF Characteristics	Symbol	Mimimum	Maximum	Units
Collector - Emitter Breakdown Voltage $I_C = 50 \text{ mAdc}$	V <sub>(BR)CEO</sub>	100		Vdc
Collector - Emitter Cutoff Current $V_{CE} = 100 \text{ Vdc}$	ICEO		100	μAdc
Collector - Emitter Cutoff Current $V_{CE} = 90 \text{ Vdc}, V_{BE} = 1.5 \text{ Vdc}$	ICEX		1.0	μAdc
Collector-8ase Cutoff Current VCB = 100 Vdc	I <sub>CBO</sub>		1.0	μAdc
Emitter - Base Cutoff Current $V_{EB} = 6.0  \text{Vdc}$	I <sub>EBO</sub>		100	μAdc
ON Characteristics				
Forward Current Transfer Ratio $I_C = 0.5$ Adc, $V_{CE} = 2.0$ Vdc $I_C = 2.0$ Adc, $V_{CE} = 2.0$ Vdc $I_C = 5.0$ Adc, $V_{CE} = 2.0$ Vdc	H <sub>FE</sub>	60 60 40	 240 	
Collector - Emitter Saturation Voltage $I_C = 2.0$ Adc, $I_B = 0.2$ Adc $I_C = 5.0$ Adc, $I_B = 0.5$ Adc	V <sub>CE(sat)</sub>		0.7 1.2	Vdc
Base - Emitter Saturation Voltage $I_C = 2.0$ Adc, $I_B = 0.2$ Adc $I_C = 5.0$ Adc, $I_B = 0.5$ Adc	V <sub>BE(sat)</sub>		1.2 1.8	Vdc





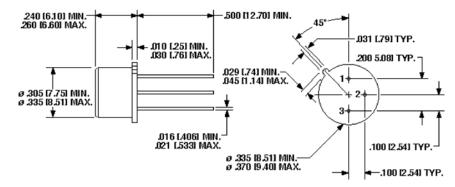
#### **Electrical Characteristics -con't**

DYNAMIC Characteristics	Symbol	Mimimum	Maximum	Units
Magnitude of Common Emitter Small-Signal Short-Circuit Forward Current Transfer Ratio $I_C = 0.5$ Adc, $V_{CE} = 10.0$ Vdc, $f = 10$ MHz	h <sub>fe</sub>	3	15	
Output Capacitance $V_{CB} = 10.0 \text{ Vdc}, I_E = 0, 100 \text{ kHz} \le f \le 1.0 \text{ MHz}$	C <sub>obo</sub>		250	pF
Input Capacitance $V_{BE} = 2.0 Vdc$ , $I_E = 0$ , 100 kHz $\leq f \leq 1.0$ MHz	C <sub>ibo</sub>		1,000	pF

#### **SAFE OPERATING AREA**

 $\begin{array}{lll} \text{DC Tests:} & & & & & & & & & & & \\ T_{C} = +25 \ ^{\circ}\text{C}, \ 1 \ \text{Cycle}, \ t = 0.5 \ \text{s} \\ \text{Test 1:} & & & & & & & & \\ V_{CE} = 2.0 \ \text{Vdc}, \ I_{C} = 5.0 \ \text{Adc} \\ \text{Test 2:} & & & & & & & \\ T_{CE} = 5.0 \ \text{Vdc}, \ I_{C} = 2.0 \ \text{Adc} \\ \text{Test 3:} & & & & & & \\ V_{CE} = 90.0 \ \text{Vdc}, \ I_{C} = 55 \ \text{mAdc} \\ \end{array}$ 

#### **Outline Drawing**



NOTE: Dimensions in Inches [mm]

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