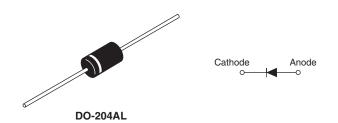
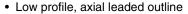


Schottky Rectifier, 1.0 A

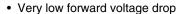


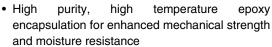
PRODUCT SUMMARY				
Package	DO-204AL (DO-41)			
I _{F(AV)}	1 A			
V_R	40 V			
V _F at I _F	0.55 V			
I _{RM} max.	12 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Single die			
E _{AS}	See Electrical table			

FEATURES











- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for commercial level
- Halogen-free according to IEC 61249-2-21 definition (-M3 only)

DESCRIPTION

The VS-1N5819... axial leaded Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	1.0	Α		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	225	Α		
V _F	1 Apk, T _J = 25 °C	0.55	V		
T _J	Range	- 40 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-1N5819	VSS-1N5819-M3	UNITS	
Maximum DC reverse voltage	V_{R}	40	40	V	
Maximum working peak reverse voltage	V_{RWM}	40	40	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	DL TEST CONDITIONS VALUES UNITS		UNITS	
Maximum average forward current See fig. 4	I _{F(AV)}	50 % duty cycle at T _L = 90 °C, rectangular waveform 1.0			
Maximum peak one cycle non-repetitive surge current	l=a	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	225	Α
non-repetitive surge current I _{FSM} See fig. 6	10 ms sine or 6 ms rect. pulse V _{RRM} applied		35		



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.6	V
		2 A		0.73	
Maximum forward voltage drop		3 A		0.9	
See fig. 1		1 A	T _J = 125 °C	0.55	
		2 A		0.63	
		3 A		0.79	
Marian Indiana		T _J = 25 °C		1.0	
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 100 °C	V_R = Rated V_R	6.0	mA
000 lig. 1		T _J = 125 °C		12	
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		60	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 8.0 n		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/μs		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽¹⁾	DC operation See fig. 4	80	°C/W
Approximate weight			0.33	g
Approximate weight			0.012	OZ.
Marking device		Case style DO-204AL (DO-41)	1N5	819

Note

 $^{^{(1)}}$ Mounted 1" square PCB, thermal probe connected to lead 2 mm from package

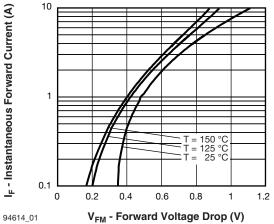


Fig. 1 - Maximum Forward Voltage Drop Characteristics

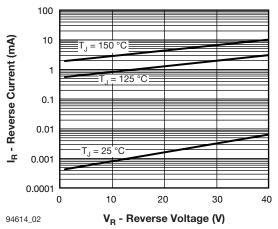


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

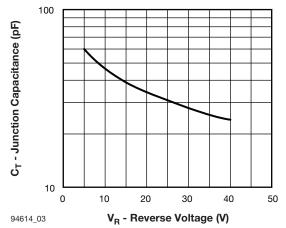


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

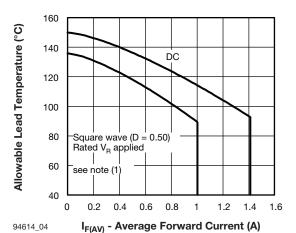


Fig. 4 - Typical Allowable Lead Temperature vs.

Average Forward Current

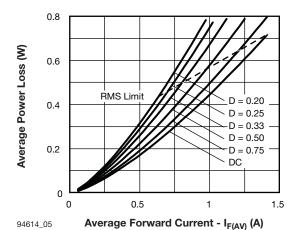


Fig. 5 - Forward Power Loss Characteristics

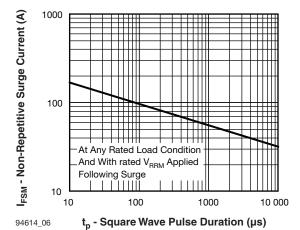


Fig. 6 - Typical Non-Repetitive Surge Current

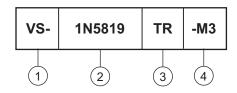
Note

Formula used: $T_C = T_J$ - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = $I_{F(AV)}$ x V_{FM} at ($I_{F(AV)}$ /D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product

- Part number: 1N5819 = 1 A, 40 V

TR = Tape and reel package

None = Bulk package

4 - Environmental digit

• None = Lead (Pb)-free and RoHS compliant

• -M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

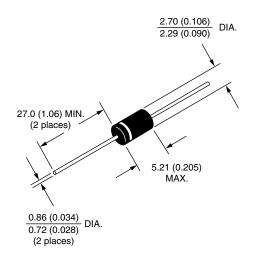
ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-1N5819	1000	1000	Bulk	
VS-1N5819TR	5000	5000	Tape and reel	
VS-1N5819-M3	1000	1000	Bulk	
VS-1N5819TR-M3	5000	5000	Tape and reel	

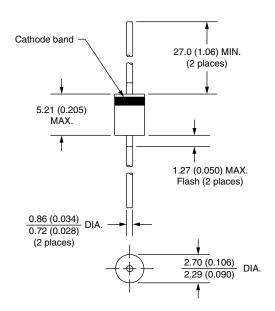
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95241</u>				
Part marking information	www.vishay.com/doc?95304			
Packaging information	www.vishay.com/doc?95338			



Axial DO-204AL (DO-41)

DIMENSIONS in millimeters (inches)







Legal Disclaimer Notice

Vishay

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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