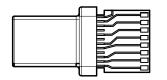
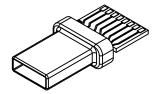
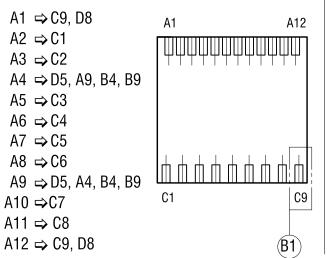


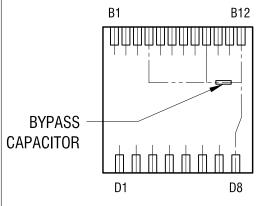
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A12	A11	A10	A9	A8	Α7	A6	A5	A4	А3	A2	A1
GND	RX2+	RX2-	V BUS	SBU1	D-	D+	CC	V BUS	TX1-	TX1+	GND
GND	TX2+	TX2-	V BUS	V CONN			SBU2	V BUS	RX1-	RX1+	GND
B1	B2	В3	B4	B5	В6	В7	В8	В9	B10	B11	B12





B1 ⇒ C9, D8 B2 ⇒ D1 B3 ⇒ D2 B4 ⇒ D5, A9, A4, B9 B5 ⇒ D3 B6 ⇒ NULL B7 ⇒ NULL B8 **⇒** D4 B9 ⇒ D5, A4, B4, B9 B10 ⇒D6 B11 ⇒ D7

B12 ⇒ C9, D8

NOTE:

1. 10n F BYPASS CAPACITOR IN BETWEEN V BUS (A4, A9, C4, C9) AND GROUND (D8)

11.08.21	CHANGE AS PER ECN 21-128	С				
	ADD C9 GROUND SOLDER PAD					
12.19.17	NEW CONTACT ARRAIGNMENT CHANGE AS PER ECN 17-078	В				
	CHANGE AS PER ECN 17-040	A				
DATE	DESCRIPTION					

KEYSTONE ELECTRONICS CORP.

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I ALLI NAME							
USB TYPE-C FUNCTIONAL PIN-OUT							
MATERIAL							
N/A							
FINISH		DRN BY	NT	3.10.17			
N/A		APP'D	RH	SCALE N/A			
TOLERANCES INCH [MM]	CODE	DWG	NO.				
DECIMAL ± .005 [± 0.13] ANGULAR ± 1° UNLESS OTHERWISE SPECIFIED	С		9	54			