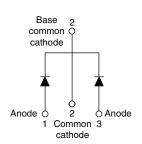


High Performance Schottky Rectifier, 2 x 20 A

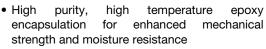




PRIMARY CHARACTERISTICS							
I _{F(AV)}	2 x 20 A						
V_{R}	45 V						
V _F at I _F	0.48 V						
I _{RM} typ.	105 mA at 125 °C						
T _J max.	150 °C						
E _{AS}	20 mJ						
Package	TO-220AB						
Circuit configuration	Common cathode						

FEATURES

- 150 °C T_J operation
- · Very low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- AEC-Q101 qualified, meets JESD 201, class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	40	Α						
V _{RRM}		45	V						
I _{FSM}	$t_p = 5 \mu s sine$	1240	Α						
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.48	V						
T _J	Range	-55 to +150	°C						

VOLTAGE RATINGS							
PARAMETER SYMBOL VS-40CTQ045HN3 UNITS							
Maximum DC reverse voltage	V_R	45	V				
Maximum working peak reverse voltage	V_{RWM}	45	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS				
Maximum average forward current per leg	average forward current per leg		20					
See fig. 5 per device	I _{F(AV)}	50 % duty cycle at T_C = 116 °C, rectangular waveform		40]			
Maximum peak one cycle non-repetitive surge current per leg	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1240	A			
See fig. 7		10 ms sine or 6 ms rect. pulse	V _{RRM} applied	350				
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 4.4 mH		20	mJ			
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \text{ x } V_R$ typical		3	А			



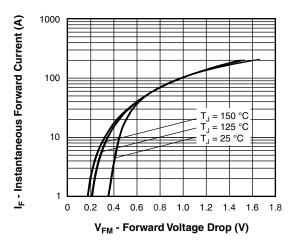
ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS			
		20 A	T _{.I} = 25 °C	0.53	V			
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	1]=25 0	0.68				
See fig. 1	VFM ('')	20 A	T 105 °C	0.48				
		40 A	T _J = 125 °C	0.67				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = rated V _B	3	mA			
iviaximum reverse leakage current per leg		T _J = 125 °C	VR = rated VR	105				
Typical reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = rated V _R	150	mA			
Threshold voltage	V _{F(TO)}	T. – T. movimum	T. T		V			
Forward slope resistance	r _t	$T_J = T_J$ maximum		8.72	m $Ω$			
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		2800	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nΗ			
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +150	°C				
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package		Б	DC eneration	2.0					
		R _{thJC}	DC operation	1.0	°C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	0.50	ì				
Approximate weight				2	g				
Approximate weight				0.07	OZ.				
Mounting torque	minimum			6 (5)	kgf · cm				
Mounting torque –	maximum			12 (10)	(lbf \cdot in)				
Marking device			Case style TO-220AB	40CTC	Q045H				





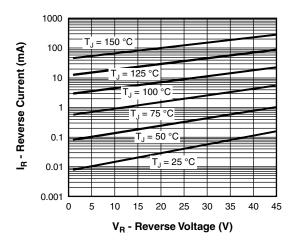


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

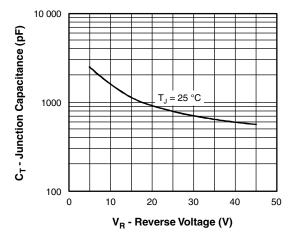


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

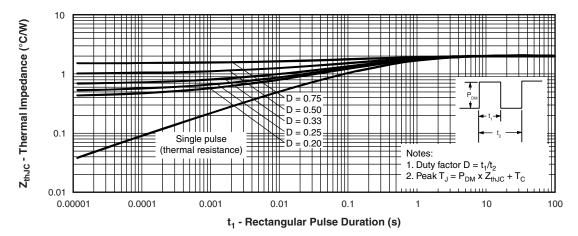


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



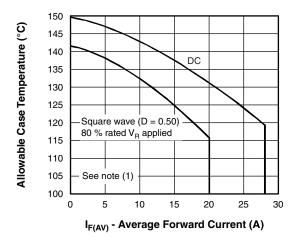


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

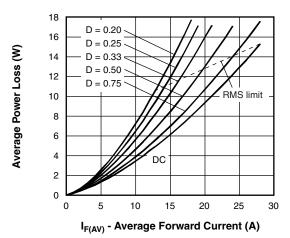


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

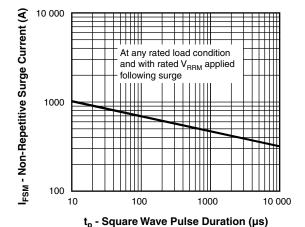


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

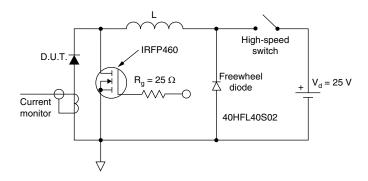


Fig. 8 - Unclamped Inductive Test Circuit

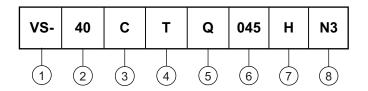
Note

Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 10 V



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 = 40 A)

3 - Circuit configuration:

C = Common cathode

4 - Package:

T = TO-220

5 - Schottky "Q" series

6 - Voltage rating (045 = 45 V)

7 - H = AEC-Q101 qualified

8 - Environmental digit

N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

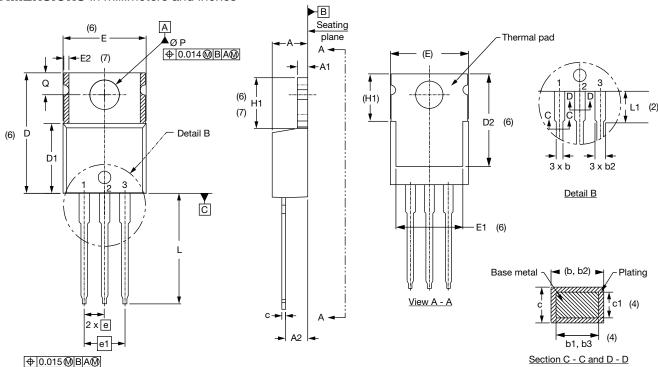
ORDERING INFORMATION (Example)								
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION								
VS-40CTQ045HN3	50	1000	Antistatic plastic tube					

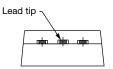
LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95222</u>						
Part marking information	www.vishay.com/doc?95028					



TO-220AB

DIMENSIONS in millimeters and inches





Conforms to JEDEC® outline TO-220AB

SYMBOL	MILLIM	IETERS	INCHES		NOTES	NOTES	HES NOTES		SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES		
Α	4.25	4.65	0.167	0.183			D2	11.68	12.88	0.460	0.507	6		
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6		
A2	2.56	2.92	0.101	0.115			E1	6.86	8.89	0.270	0.350	6		
b	0.69	1.01	0.027	0.040			E2	-	0.76	-	0.030	7		
b1	0.38	0.97	0.015	0.038	4		е	2.41	2.67	0.095	0.105			
b2	1.20	1.73	0.047	0.068			e1	4.88	5.28	0.192	0.208			
b3	1.14	1.73	0.045	0.068	4		H1	5.84	6.86	0.230	0.270	6, 7		
С	0.36	0.61	0.014	0.024			L	13.52	14.02	0.532	0.552			
c1	0.36	0.56	0.014	0.022	4		L1	3.32	3.82	0.131	0.150	2		
D	14.85	15.25	0.585	0.600	3		ØΡ	3.54	3.73	0.139	0.147			
D1	8.38	9.02	0.330	0.355			Q	2.60	3.00	0.102	0.118			

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC® TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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Vishay

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