

# 74AUP3G16

Low-power triple buffer

Rev. 2 — 12 October 2016

Product data sheet

## 1. General description

The 74AUP3G16 is a triple buffer.

Schmitt trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device ensures a very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing a damaging backflow current through the device when it is powered down.

## 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
  - ◆ JESD8-12 (0.8 V to 1.3 V)
  - ◆ JESD8-11 (0.9 V to 1.65 V)
  - ◆ JESD8-7 (1.2 V to 1.95 V)
  - ◆ JESD8-5 (1.8 V to 2.7 V)
  - ◆ JESD8-B (2.7 V to 3.6 V)
- ESD protection:
  - ◆ HBM JESD22-A114F Class 3A exceeds 5000 V
  - ◆ MM JESD22-A115-A exceeds 200 V
  - ◆ CDM JESD22-C101E exceeds 1000 V
- Low static power consumption;  $I_{CC} = 0.9 \mu A$  (maximum)
- Latch-up performance exceeds 100 mA per JESD 78B Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of  $V_{CC}$
- $I_{OFF}$  circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

**nexperia**

### 3. Ordering information

**Table 1. Ordering information**

Type number	Package				Version
	Temperature range	Name	Description		
74AUP3G16DC	−40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm		SOT765-1
74AUP3G16GF	−40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 × 1 × 0.5 mm		SOT1089
74AUP3G16GM	−40 °C to +125 °C	XQFN8	plastic, extremely thin quad flat package; no leads; 8 terminals; body 1.6 × 1.6 × 0.5 mm		SOT902-2

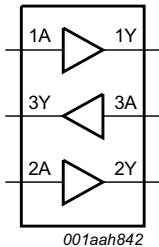
### 4. Marking

**Table 2. Marking codes**

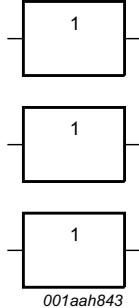
Type number	Marking code <sup>[1]</sup>
74AUP3G16DC	5A
74AUP3G16GF	5A
74AUP3G16GM	5A

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

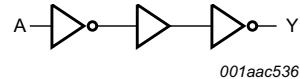
### 5. Functional diagram



**Fig 1. Logic symbol**



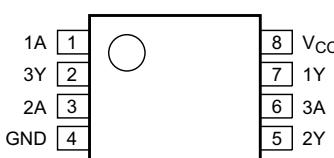
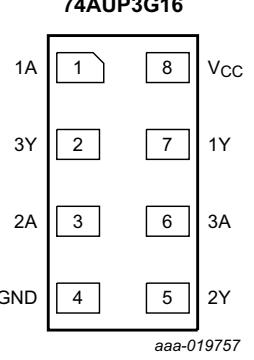
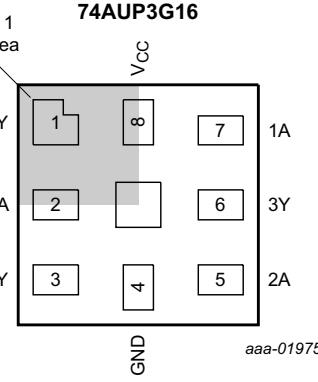
**Fig 2. IEC logic symbol**



**Fig 3. Logic diagram (one gate)**

## 6. Pinning information

### 6.1 Pinning

 <p><b>74AUP3G16</b></p> <p>aaa-019756</p>	 <p><b>74AUP3G16</b></p> <p>1A      8      VCC 3Y      2      7      1Y 2A      3      6      3A GND    4      5      2Y</p> <p>aaa-019757</p> <p>Transparent top view</p>	 <p><b>74AUP3G16</b></p> <p>1Y      8      V<sub>CC</sub> 3A      2      6      3Y 2Y      3      4      2A</p> <p>GND    1A aaa-019759</p> <p>Transparent top view</p>
<p><b>Fig 4. Pin configuration SOT765-1</b></p>	<p><b>Fig 5. Pin configuration SOT1089</b></p>	<p><b>Fig 6. Pin configuration SOT902-2</b></p>

### 6.2 Pin description

**Table 3. Pin description**

Symbol	Pin		Description
	SOT765-1, SOT1089	SOT902-2	
1A, 2A, 3A	1, 3, 6	7, 5, 2	data input
1Y, 2Y, 3Y	7, 5, 2	1, 3, 6	data output
GND	4	4	ground (0 V)
V <sub>CC</sub>	8	8	supply voltage

## 7. Functional description

**Table 4. Function table<sup>[1]</sup>**

Input	Output
nA	nY
L	L
H	H

[1] H = HIGH voltage level; L = LOW voltage level.

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit	
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V	
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V	-50	-	mA	
V <sub>I</sub>	input voltage		[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V	-50	-	mA	
V <sub>O</sub>	output voltage	Active mode and Power-down mode	[1]	-0.5	+4.6	V
I <sub>O</sub>	output current	V <sub>O</sub> = 0 V to V <sub>CC</sub>	-	±20	mA	
I <sub>CC</sub>	supply current		-	+50	mA	
I <sub>GND</sub>	ground current		-50	-	mA	
T <sub>STG</sub>	storage temperature		-65	+150	°C	
P <sub>TOT</sub>	total power dissipation	T <sub>AMB</sub> = -40 °C to +125 °C	[2]	-	250 mW	

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For VSSOP8 packages: above 110 °C the value of P<sub>TOT</sub> derates linearly with 8.0 mW/K.

For XSON8 and XQFN8 packages: above 118 °C the value of P<sub>TOT</sub> derates linearly with 7.8 mW/K.

## 9. Recommended operating conditions

**Table 6. Operating conditions**

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
V <sub>I</sub>	input voltage		0	3.6	V
V <sub>O</sub>	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>AMB</sub>	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	-	200	ns/V

## 10. Static characteristics

**Table 7. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>T<sub>amb</sub> = 25 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.72	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.1	µA
	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.2	µA
ΔI <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.2	µA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.5	µA
ΔI <sub>CC</sub>	additional supply current	V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 3.3 V	-	-	40	µA
C <sub>I</sub>	input capacitance	V <sub>CC</sub> = 0 V to 3.6 V; V <sub>I</sub> = GND or V <sub>CC</sub>	-	1.0	-	pF
C <sub>O</sub>	output capacitance	V <sub>O</sub> = GND; V <sub>CC</sub> = 0 V	-	1.8	-	pF

**Table 7. Static characteristics ...continued**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>T<sub>amb</sub> = -40 °C to +85 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.67	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.55	-	-	V
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.5	µA
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.5	µA
ΔI <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.6	µA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.9	µA
ΔI <sub>CC</sub>	additional supply current	V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 3.3 V	-	-	50	µA

**Table 7. Static characteristics ...continued**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>T<sub>amb</sub> = -40 °C to +125 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.75V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.25V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.75	µA
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.75	µA
ΔI <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.75	µA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	1.4	µA
ΔI <sub>CC</sub>	additional supply current	V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 3.3 V	-	-	75	µA

## 11. Dynamic characteristics

**Table 8. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit, see [Figure 8](#).

Symbol	Parameter	Conditions	25 °C			−40 °C to +125 °C			Unit
			Min	Typ <sup>[1]</sup>	Max	Min	Max (85 °C)	Max (125 °C)	
<b>C<sub>L</sub> = 5 pF</b>									
t <sub>pd</sub>	propagation delay	nA to nY; see <a href="#">Figure 7</a> [2]							
		V <sub>CC</sub> = 0.8 V	-	14.9	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.6	4.7	9.2	2.0	10.0	11.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.1	3.4	5.7	1.6	6.5	7.2	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.8	2.9	4.5	1.4	5.2	5.8	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.5	2.3	3.5	1.2	4.2	4.6	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.4	2.1	3.2	1.0	3.8	4.2	ns
<b>C<sub>L</sub> = 10 pF</b>									
t <sub>pd</sub>	propagation delay	nA to nY; see <a href="#">Figure 7</a> [2]							
		V <sub>CC</sub> = 0.8 V	-	18.4	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.2	5.6	10.9	2.3	11.8	13.1	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.6	4.1	6.7	1.9	7.7	8.5	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.3	3.4	5.3	1.7	6.2	6.9	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.0	2.9	4.2	1.5	5.0	5.5	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.7	2.6	3.8	1.4	4.6	5.1	ns
<b>C<sub>L</sub> = 15 pF</b>									
t <sub>pd</sub>	propagation delay	nA to nY; see <a href="#">Figure 7</a> [2]							
		V <sub>CC</sub> = 0.8 V	-	21.9	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.6	6.4	12.6	2.6	13.8	15.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.0	4.6	7.6	2.2	8.9	9.8	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.6	3.9	6.0	2.0	7.2	7.9	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.3	3.3	4.8	1.8	5.7	6.3	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.1	3.1	4.2	1.6	5.0	5.5	ns
<b>C<sub>L</sub> = 30 pF</b>									
t <sub>pd</sub>	propagation delay	nA to nY; see <a href="#">Figure 7</a> [2]							
		V <sub>CC</sub> = 0.8 V	-	32.1	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.8	8.7	16.3	3.6	18.9	20.8	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	4.0	6.2	10.3	3.4	12.2	13.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.6	5.2	8.1	3.2	9.8	10.8	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	3.0	4.4	6.4	2.7	7.7	8.5	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.9	4.2	5.6	2.5	6.5	7.2	ns

**Table 8. Dynamic characteristics ...continued**Voltages are referenced to GND (ground = 0 V); for test circuit, see [Figure 8](#).

Symbol	Parameter	Conditions	25 °C			−40 °C to +125 °C			Unit
			Min	Typ <sup>[1]</sup>	Max	Min	Max (85 °C)	Max (125 °C)	
<b>C<sub>L</sub> = 5 pF, 10 pF, 15 pF and 30 pF</b>									
C <sub>PD</sub>	power dissipation capacitance	f <sub>i</sub> = 1 MHz; V <sub>I</sub> = GND to V <sub>CC</sub> <sup>[3][4]</sup>	-	2.5	-	-	-	-	pF
		V <sub>CC</sub> = 0.8 V	-	2.6	-	-	-	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	2.7	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	2.9	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	3.4	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	4.0	-	-	-	-	pF

[1] All typical values are measured at nominal V<sub>CC</sub>.[2] t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>.

[3] All specified values are the average typical values over all stated loads.

[4] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in  $\mu$ W).

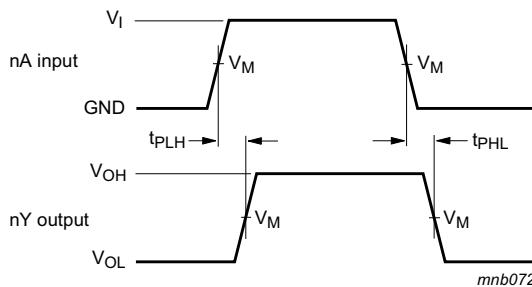
$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

f<sub>i</sub> = input frequency in MHz;f<sub>o</sub> = output frequency in MHz;C<sub>L</sub> = load capacitance in pF;V<sub>CC</sub> = supply voltage in V;

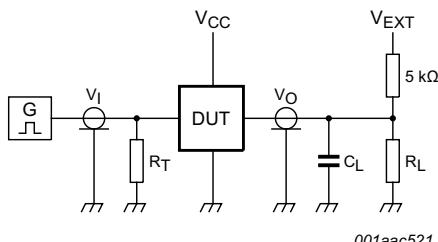
N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

## 12. Waveforms

Measurement points are given in [Table 9](#).Logic levels: V<sub>OL</sub> and V<sub>OH</sub> are typical output voltage levels that occur with the output load.**Fig 7. The data input (nA) to output (nY) propagation delays****Table 9. Measurement points**

Supply voltage	Output	Input		
V <sub>CC</sub> 0.8 V to 3.6 V	V <sub>M</sub> 0.5V <sub>CC</sub>	V <sub>M</sub> 0.5V <sub>CC</sub>	V <sub>I</sub> V <sub>CC</sub>	t <sub>r</sub> = t <sub>f</sub> ≤ 3.0 ns



Test data is given in [Table 10](#).

Definitions for test circuit:

$R_L$  = Load resistance.

$C_L$  = Load capacitance including jig and probe capacitance.

$R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator.

$V_{EXT}$  = External voltage for measuring switching times.

**Fig 8. Test circuit for measuring switching times**

**Table 10. Test data**

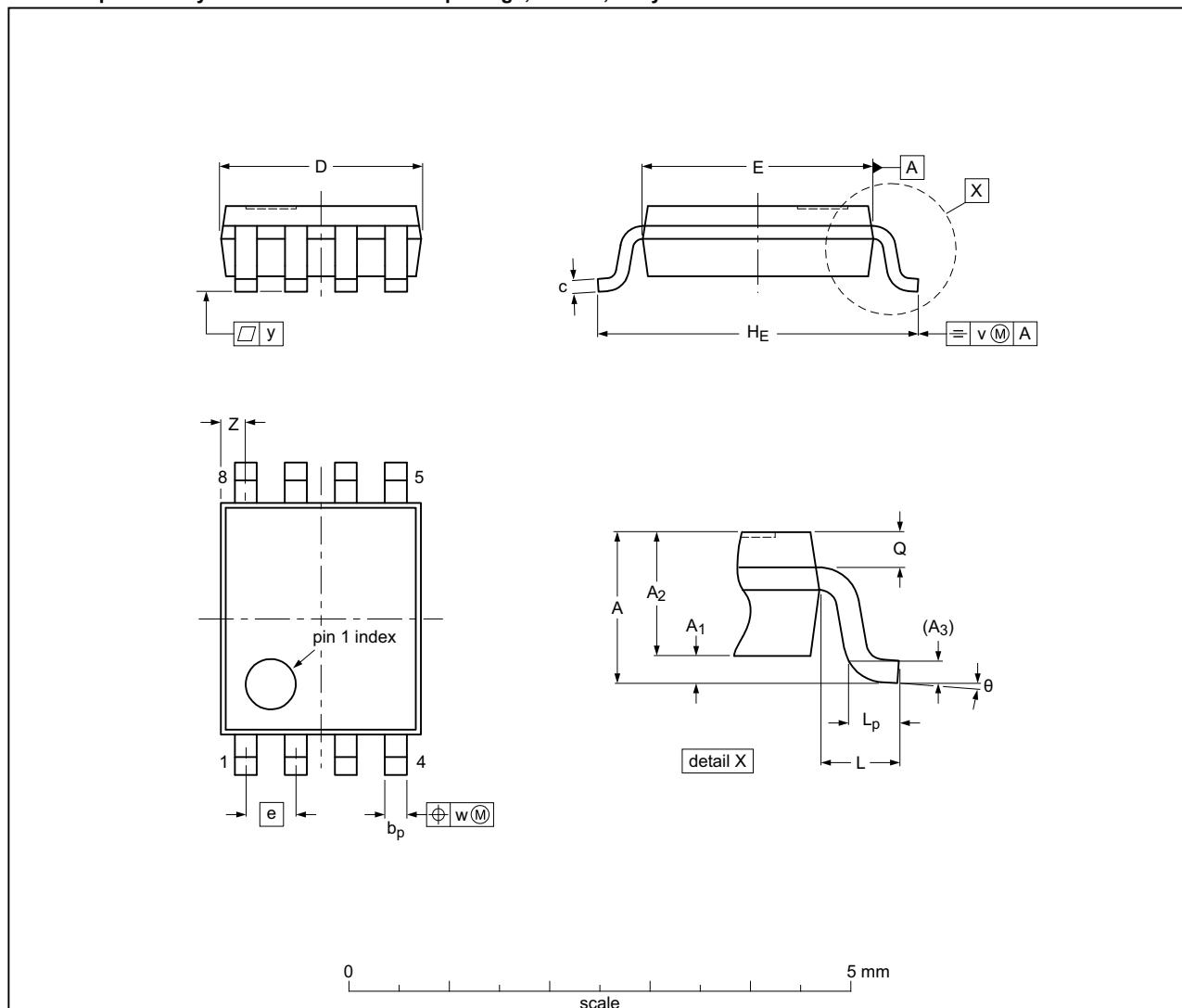
Supply voltage	Load	$V_{EXT}$	$t_{PLH}, t_{PHL}$	$t_{PZH}, t_{PHZ}$	$t_{PZL}, t_{PLZ}$
$V_{CC}$	$C_L$	$R_L$ <sup>[1]</sup>			
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF, and 30 pF	5 k $\Omega$ or 1 M $\Omega$	open	GND	2 $V_{CC}$

[1] For measuring enable and disable times  $R_L = 5 \text{ k}\Omega$ ., for measuring propagation delays, setup and hold times and pulse width  $R_L = 1 \text{ M}\Omega$ .

## 13. Package outline

VSSOP8: plastic very thin shrink small outline package; 8 leads; body width 2.3 mm

SOT765-1



Dimensions (mm are the original dimensions)

Unit	A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	b <sub>p</sub>	c	D <sup>(1)</sup>	E <sup>(2)</sup>	e	H <sub>E</sub>	L	L <sub>p</sub>	Q	v	w	y	Z <sup>(1)</sup>	θ
mm	max	0.15	0.85		0.27	0.23	2.1	2.4		3.2	0.40	0.21		0.4	0.21		0.4	8°
mm	nom	1			0.12				0.5		0.4			0.2	0.08	0.1		
mm	min	0.00	0.60		0.17	0.08	1.9	2.2		3.0	0.15	0.19				0.1	0.1	0°

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

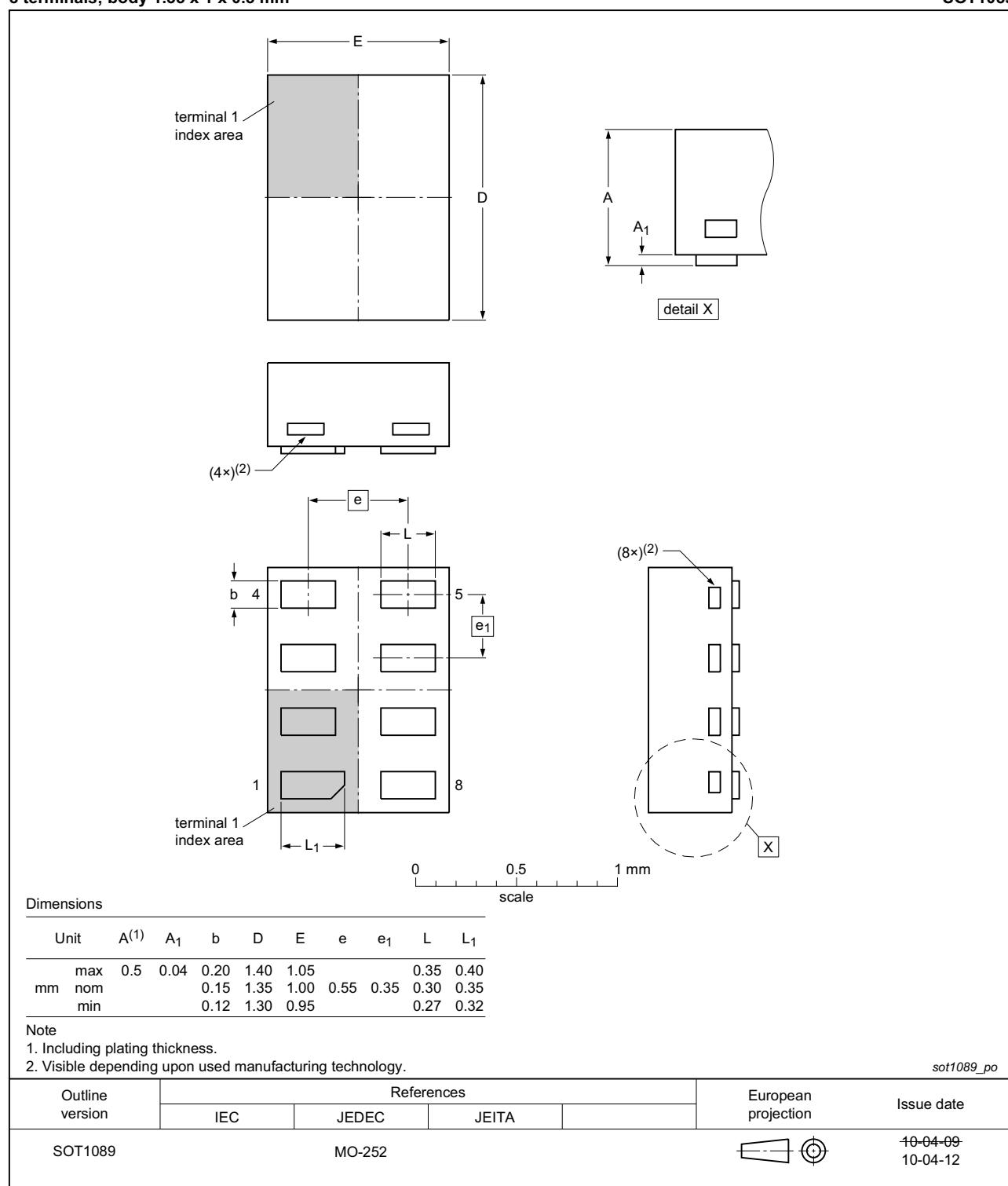
sot765-1\_po

Outline version	References			European projection	Issue date
	IEC	JEDEC	JEITA		
SOT765-1	MO-187				-07-06-02- 16-05-31

Fig 9. Package outline SOT765-1 (VSSOP8)

**XSON8: extremely thin small outline package; no leads;  
8 terminals; body 1.35 x 1 x 0.5 mm**

SOT1089



**Fig 10. Package outline SOT1089 (XSON8)**

XQFN8: plastic, extremely thin quad flat package; no leads;  
8 terminals; body 1.6 x 1.6 x 0.5 mm

SOT902-2

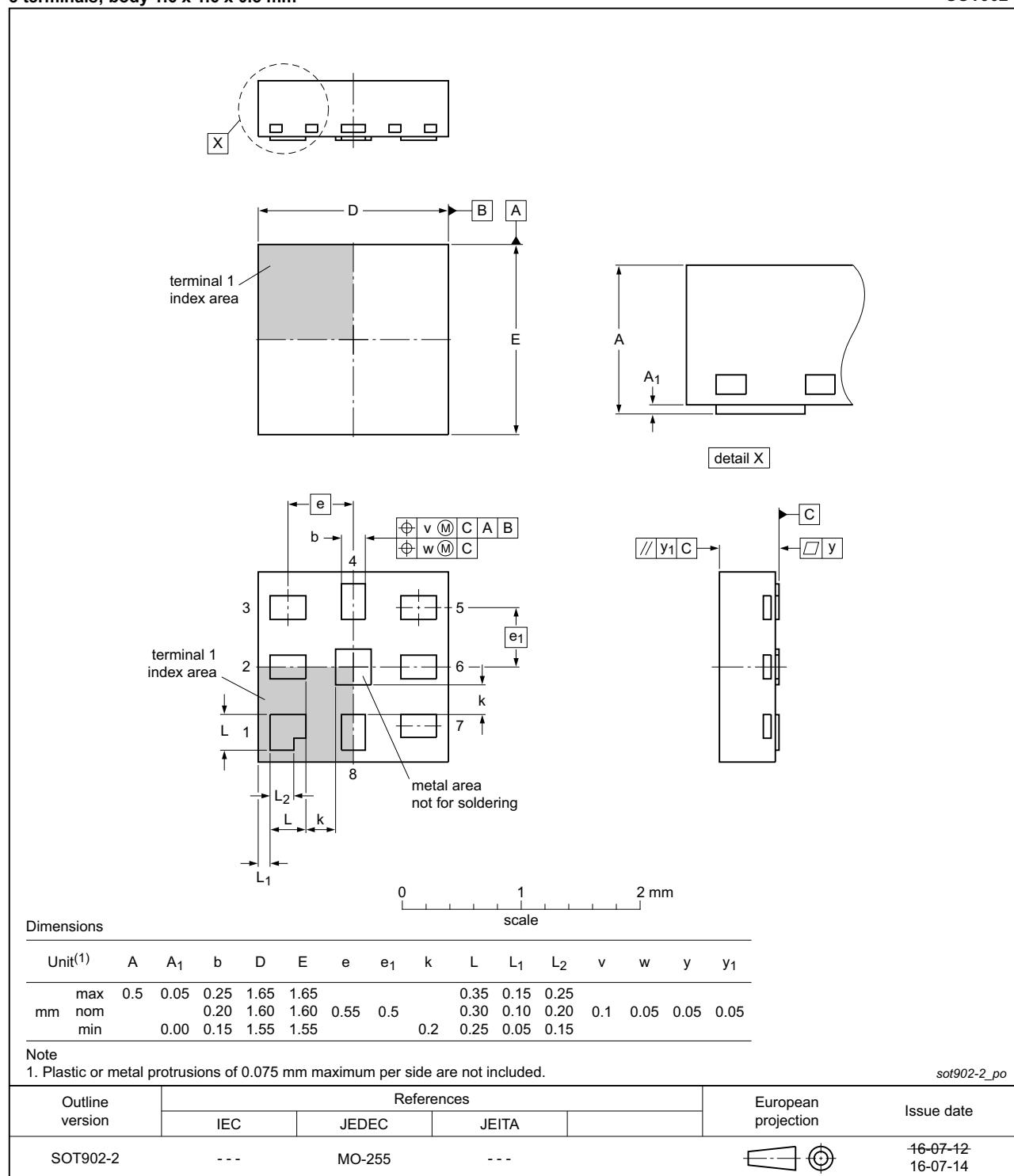


Fig 11. Package outline SOT902-2 (XQFN8)

## 14. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

## 15. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP3G16 v.2	20161012	Product data sheet	-	74AUP3G16 v.1
Modifications:	<ul style="list-style-type: none"><li>Type numbers 74AUP3G16DP, 74AUP3G16GD, 74AUP3G16GN, 74AUP3G16GS and 74AUP3G16GT removed.</li></ul>			
74AUP3G16 v.1	20151103	Product data sheet	-	-

## 16. Legal information

### 16.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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## 18. Contents

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<b>1</b>	<b>General description</b> .....	<b>1</b>
<b>2</b>	<b>Features and benefits</b> .....	<b>1</b>
<b>3</b>	<b>Ordering information</b> .....	<b>2</b>
<b>4</b>	<b>Marking</b> .....	<b>2</b>
<b>5</b>	<b>Functional diagram</b> .....	<b>2</b>
<b>6</b>	<b>Pinning information</b> .....	<b>3</b>
6.1	Pinning .....	3
6.2	Pin description .....	3
<b>7</b>	<b>Functional description</b> .....	<b>3</b>
<b>8</b>	<b>Limiting values</b> .....	<b>4</b>
<b>9</b>	<b>Recommended operating conditions</b> .....	<b>4</b>
<b>10</b>	<b>Static characteristics</b> .....	<b>5</b>
<b>11</b>	<b>Dynamic characteristics</b> .....	<b>8</b>
<b>12</b>	<b>Waveforms</b> .....	<b>9</b>
<b>13</b>	<b>Package outline</b> .....	<b>11</b>
<b>14</b>	<b>Abbreviations</b> .....	<b>14</b>
<b>15</b>	<b>Revision history</b> .....	<b>14</b>
<b>16</b>	<b>Legal information</b> .....	<b>15</b>
16.1	Data sheet status .....	15
16.2	Definitions.....	15
16.3	Disclaimers.....	15
16.4	Trademarks.....	16
<b>17</b>	<b>Contact information</b> .....	<b>16</b>
<b>18</b>	<b>Contents</b> .....	<b>17</b>