

VF901723-38.400MHz VCTCXO Low Noise, LVCMOS

Features

- 5G mmWave reference design
- 14x13x3 mm SMD
- Output Frequency 38.400 MHz
- Ultra-low jitter and phase noise
- Excellent frequency stability <0.280 ppm



5G Communications Reference



The VF901723 is a low noise TCXO which provides a LVCMOS output frequency at 38.400 MHz. The temperature stability is less than ± 0.280 ppm over a temperature range of -40°C to +85°C. The VF901723 is available in a 14 x 13 x 3 mm surface mount package.

Electrical Specifications

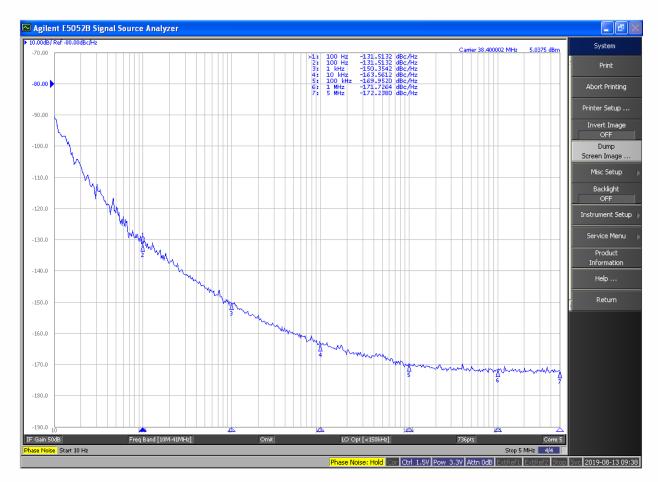
Parameter	Conditions & Remarks	Min	Typical	Max	Unit
Frequency	F _{NOM}		38.400		MHz
Freq. Stability vs. Temp	Δ F/F; -40 to +85°C ($F_{max} - F_{min}$)/2	-	-	±0.280	ppm
Freq. Stability vs. Supply Voltage	ΔF/ F _{NOM}	-	±0.1	-	ppm/V
Freq. Stability vs. Aging	Per year – first year	-	±1	-	ppm
	10 years	-	±3	-	ppm
Operating Temperature Range	Та	-40	-	+85	°C
Supply Voltage	Vcc	3.15	3.3	3.45	V
Voltage Control	Vc	0	-	3.3	V
Input Impedance	Z _{IN}	100	-	-	ΚΩ
APR	Sufficient range to correct for initial calibration, 15 yrs aging, temperature, voltage, and load variations	±5	-	-	ppm
Deviation Slope	Positive, monotonic				
Linearity		-	-	10	%
Modulation BW	3dB BW	-	6	-	Hz
Input Current	Icc	-	-	50	mA

Rev. K_2208 WWW.ctscorp.com Page 1 of 3

VCTCXO Low Noise, LVCMOS

Electrical Specifications

Parameter	Conditions & Remarks	Min	Typical	Max	Unit
Output – LVCMOS					
Output Level	Voh	0.9 Vcc	-	Vcc	V
	VoL	0	-	0.2	V
Output Load	Z_L @ lout = 8 mA (max)	-	-	10	pf
Duty Cycle	@ 50% Vp-p	45	50	55	%
Rise/Fall times	T _R 10% - 90%	-	-	3.5	nsec
	T _F 90% - 10%	-	-	3.5	
Start-up time		-	2	3	sec
SSB Phase Noise (38.400MHz)	Offset = 10Hz	-	-93	-	
	100Hz	-	-131	-	
	1KHz	-	-150	-	dBc/Hz
	10KHz	-	-163	-	
	100KHz	-	-169	-	
	1MHz	-	-172	-	



Typical phase noise performance, 38.400MHz



VCTCXO Low Noise, LVCMOS

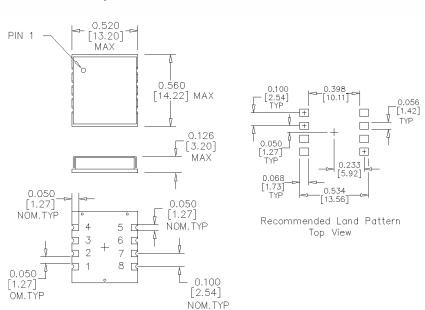
Absolute Maximum Ratings

Parameter	Conditions & Remarks	Min	Typical	Max	Unit
Supply Breakdown Voltage	V _{CC}	-0.5	-	3.65	V
Storage Temperature	Ts	-45	-	+90	°C
Control Voltage	Vc	-0.5	-	3.3	V

Mechanical and Environmental

Mechanical Shock	Per MIL-STD-202, Method 213, condition E
Thermal Shock	Per MIL-STD-883, Method 1011, condition A
Vibration	Per MIL-STD-883, Method 2007, condition A
Soldering Conditions	260°C for 10s max
Hermetic Seal	Leak rate less than 5x10 ⁻⁸ atm.cc/s of helium (crystal only)
Markings	Laser engraved or epoxy ink

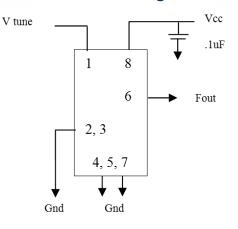
Mechanical Specification



Pin Assignments

ction		
V tune (V _C)		
nd		
UT		
nd		
CC		

Connection Diagram



Marking

Approved

CTS VF901723 38.400MHz Date Code

or

CTS VF901775 38.400MHz Date Code

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