DOLIM	PRODUCTS	TYP	E	F	PAGE
ROHM	Semiconducto	or IC	BH1425KN		
TRUCTURE RODUCT SERIES YPE EATURE	•	k LSI for Mobile P t Mode I ² C-BUS in wideband PLL free	terface. juency synthesize	Transmitter) er (76MHz~108MHz).	
• Absolute Maximum Rating	Possible to operative operation of the sound of the sound operation oper	et transmission por et pre-emphasis tir ormance Low-pase e system FM stered n frequency is stat ate in monaural m	wer by serial cont ne constant by se s Filter. o modulator circui le because it has	rial control.	circuit.
Parameter	Symbol	Limits	Unit	Condition	
Supply voltage	VCC	-0.3 to +5.5	V	Pin 2, 6, 11, 13, 23, 25	
Data input voltage 1	VIN-D1	-0.3 to V _{DDIO} +0	3 V	Pin 16, 19, 20	
Data input voltage 2	V _{IN-D2}	-0.3 to +5.5	v	Pin 17, 18	
Power dissipation	Pd	370	mW	(Note 1)	
Storage temperature	Tstg	-55 to +125			
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The Japanese version of this docum version. If there are any differences i opplication example ROHM cannot provide adequate of The product described in this sp office-automation equipment, co Should you intend to use this pr	n translation version of this confirmation of patents. ecification is designed to mmunications devices, el roduct with equipment of	document, formal version to be used with ordina dectrical appliances, and r devices which requi	on takes priority. ry electronic equipm nd electronic toys). re an extremely high	level of reliability and the malfur	ual equip
The Japanese version of this docum version. If there are any differences i pplication example ROHM cannot provide adequate of The product described in this sp office-automation equipment, con Should you intend to use this pr would directly endanger human controllers and other safety device ROHM assumes no responsibilit representations that the circuits a DESIGN CHECK	n translation version of this confirmation of patents. recification is designed to mmunications devices, el roduct with equipment oi life (such as medical inst ces), please be sure to co ty for use of any circuit are free from patent infrir	document, formal version be used with ordinal lectrical appliances, and r devices which requi truments, transportation sult with our sales ro is described herein, of agement. E : 2006/05/08	on takes priority. ry electronic equipm nd electronic toys). re an extremely high on equipment, aeros epresentative in adva sonveys no license of SPECIFICATION N	level of reliability and the malfur bace machinery, nuclear-reactor on nce.	ual equip action of controllers and make

○ Operating Supply Voltage Range

Parameter	Symbol	Limits	Unit	Conditions
Operating supply voltage 1	Vcc	2.7 to 4.0	v	Pin 2, 6, 11, 23, 25
Operating supply voltage 2	VDDIO	1.7 to 4.0	v	Pin 13
Operating temperature	Tpor	-20 to +85	°C	
Audio input level	V _{IN-A}	to -10	dBV	Pin 26, 27
Audio input frequency	f _{IN-A}	20 to 15k	Hz	Pin 26, 27
Transmission frequency	f _{TX}	76.0 to 108.0	MHz	100kHz step
Control terminal "H" level input voltage 1	V _{IH1}	0.7VDDIO to VDDIO	v	Pin 16, 19, 20
Control terminal "H" level input voltage 2	V _{IH2}	0.7V _{DDIO} to +5.5	v	Pin 17, 18
Control terminal "L" level input voltage	VIL	GND to 0.3VDDIO	V	Pin 16, 17, 18, 19, 20

TYPE

○ Electrical Characteristics

Unless otherwise specified Ta=25°C, V_{CC}=3.0V, V_{DDIO}=1.8V

Signal source : $f_{IN}=1kHz$, $V_{IN}=-20dBV$ Common condition : $f_{TX}=90MHz$, $\Delta f=\pm75kHz$, $\tau=50 \,\mu$ s

Devementer	Cumbel	Limits					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition	
Quiescent current	lα	14	20	28	mA	Tx power control is 0dB setting.	
Power down current	IPWD	-	0	1	μA	BUSEN="L"	
Channel separation	Sep	25	40	-	dB	L→R, R→L	
Signal to noise ratio	SNR	53	61	-	dB	L+R	
		54	62	-	dB	MONO	
Total harmonic distortion	THD	-	0.1	0.3	%	L+R	
		—	0.1	0.3	%	MONO	
Transmission power level	Ρτχ	-9	-6	-3	dBm	Tx power control is 0dB setting.	
Pilot modulation rate	M _P .	7	11	15	%	L+R	
"H" level input current	In	-	_	1.0	μA	Pin 16, 19, 20 V _{IN} =3V	
"L" level input current	I _{IL}	-1.0	_	-	μA	Pin 16, 19, 20 V _{IN} =0V	
"L" level output voltage	Vol	-	_	0.2VDDIO	v	Pin 18 I ₀ =3mA	

◎ This product is not designed for protection against radioactive rays.

REV. :

◎ The specification of transmission output level be based on the Radio Law in every country and the area.

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	PRODUCTS Semiconductor IC	TYPE	BH1425K	(N		PAGE 3/4		
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0.22±0.05 00 00 000								
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2000 200 2000 2								
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	0.6-0.3							
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		8.12) 8.12)						
	0.5	<u><</u>						
	VQFN28							
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			number • Pin r	ame				
TEST XSEL BUSEN	SDA SCL XIN	No.	Name	No.	Name			
21 20 19	18 17 16 15	1	TEST	15	XIN			
TEST		2	VCCAF	16 17	ADRS			
	C-BUS Interface	4	GNDAF	18	SDA			
	Control Logic System Clock 13 VDDIO	5	LOOPFIL	19	BUSEN			
GNDPA 24 8 12	§ 12 VSS	6	VCCVCO	20	XSEL			
		7	GNDVCO	21	TEST			
		8	TANK1	22	PAOUT			
AFL 26 ATT Pre-Emphasis Limiter	Stereo	9	VC	23	VCCPA2			
AFR 27 ATT Pre-Emphasis	Encoder Modulation VCO P 9 VC	10	TANK2	24	GNDPA			
	Level Adjuster	11	VDD	25	VCCPA1			
	B TANK1	12	VSS	26	AFL			
TEST VCCAF GNDAF	4 5 6 7	13	VDDIO XOUT	27 28	AFR VREF			
			7001]		
TEST VCCAF GNDAF	LPFSW VCCVCO GNDVCO							
ROHM CO., LTD.	REV. : A	SPECIFICAT	ION No. :	TSZ022	201-BH1425	5KN-1-2		

TSZ22111.05



- Cautions on use
- (1) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

TYPE

(2) GND potential

Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.

(3) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.

(4) Shorts between pins and misinstallation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is misinstalled and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.

(5) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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