

# **Standard Recovery Diodes,** (Stud Version), 400 A

#### **FEATURES**

- Wide current range
- High voltage ratings up to 2400 V
- High surge current capabilities
- Stud cathode and stud anode version
- Standard JEDEC® types
- · Compression bonded encapsulations
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



### TYPICAL APPLICATIONS

- Converters
- Power supplies
- · Machine tool controls
- · High power drives
- · Medium traction applications

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	400 A			
Package	DO-9 (DO-205AB)			
Circuit configuration	Single			

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
		480	A		
I <sub>F(AV)</sub>	T <sub>C</sub>	120	°C		
I <sub>F(RMS)</sub>		630			
I <sub>FSM</sub>	50 Hz	8250	Α		
	60 Hz	8640			
l <sup>2</sup> t	50 Hz	340	kA <sup>2</sup> s		
	60 Hz	311	KA-S		
V <sub>RRM</sub>	Range	1600 to 2400	V		
TJ		-40 to +190	°C		

#### **ELECTRICAL SPECIFICATIONS**

VOLTAGE RATINGS						
TYPE NUMBER	VOLTAGE CODE VRRM, MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V		V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	$\begin{aligned} & I_{RRM} \text{ MAXIMUM} \\ \text{AT T}_{J} &= T_{J} \text{ MAXIMUM} \\ & \text{mA} \end{aligned}$		
	16	1600	1700			
VS-SD400N/R	20	2000	2100	15		
	24	2400	2500			



FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
				400	Α	
Maximum average forward current		180° conduction, half sine wave		120	°C	
at case temperature	I <sub>F(AV)</sub>	180° condi	uction, nair sine	e wave	480	Α
					100	°C
Maximum RMS forward current	I <sub>F(RMS)</sub>	DC at 110	°C case tempe	rature	630	
		t = 10 ms	No voltage	Sinusoidal half wave, initial T <sub>J</sub> = T <sub>J</sub> maximum	8250	]
Maximum peak, one-cycle forward,		t = 8.3  ms	reapplied		8640	Α
non-repetitive surge current	I <sub>FSM</sub>	t = 10 ms	100 % V <sub>RRM</sub>		6940	
		t = 8.3 ms	reapplied		7270	
	l <sup>2</sup> t	t = 10 ms	No voltage		340	kA <sup>2</sup> s
Maximum I <sup>2</sup> t for fusing		t = 8.3  ms	reapplied		311	
Maximum i-t for fusing		t = 10 ms	100 % V <sub>RRM</sub> reapplied		241	
		t = 8.3  ms		220		
Maximum I <sup>2</sup> √t for fusing	I <sup>2</sup> √t	t = 0.1 to 10 ms, no voltage reapplied		3400	kA²√s	
Low level value of threshold voltage	V <sub>F(TO)1</sub>	(16.7 % x $\pi$ x $I_{F(AV)}$ < $I$ < $\pi$ x $I_{F(AV)}$ ), $I_J = I_J$ maximum		0.80	V	
High level value of threshold voltage	V <sub>F(TO)2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		0.85		
Low level value of forward slope resistance	r <sub>f1</sub>	(16.7 % x $\pi$ x $I_{F(AV)}$ < $I$ < $\pi$ x $I_{F(AV)}$ ), $T_J = T_J$ maximum		0.55	- mW	
High level value of forward slope resistance	r <sub>f2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		0.51	11100	
Maximum forward voltage drop	V <sub>FM</sub>	$I_{pk}$ = 1500 A, $T_J$ = $T_J$ maximum, $t_p$ = 10 ms sinusoidal wave		1.62	V	

THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		UNITS
Maximum junction operating temperature range	Tu		-40 to +190	°C
Maximum storage temperature range	T <sub>Stg</sub>		-55 to +200	
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation	0.11	K/W
Maximum thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth, flat and greased	0.04	<b>I</b> ∕√ VV
Maximum allowed mounting torque ± 10 %		Not-lubricated threads	27	Nm
Approximate weight			250	g
Case style		See dimensions (link at the end of datasheet DO-9 (DO		-205AB)

△R <sub>thJC</sub> CONDUCTION				
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS
180°	0.020	0.013		
120°	0.023	0.023		
90°	0.029	0.031	$T_J = T_J \text{ maximum}$	K/W
60°	0.042	0.044		
30°	0.073	0.074		

#### Note

• The table above shows the increment of thermal resistance RthJC when devices operate at different conduction angles than DC

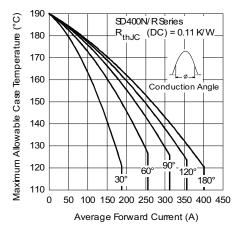


Fig. 1 - Current Ratings Characteristics

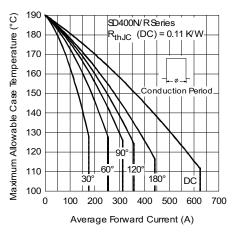


Fig. 2 - Current Ratings Characteristics

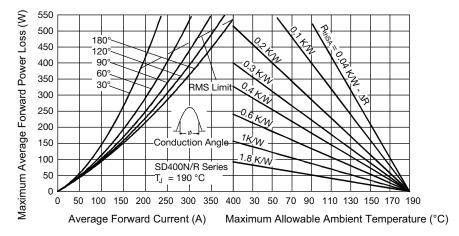


Fig. 3 - Forward Power Loss Characteristics

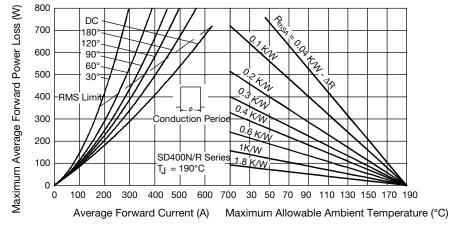


Fig. 4 - Forward Power Loss Characteristics

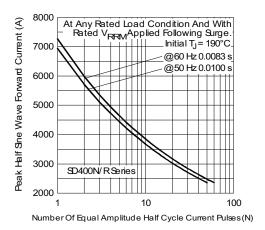


Fig. 5 - Maximum Non-Repetitive Surge Current

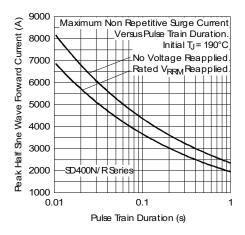


Fig. 6 - Maximum Non-Repetitive Surge Current

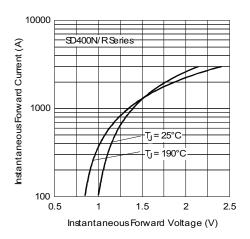


Fig. 7 - Forward Voltage Drop Characteristics

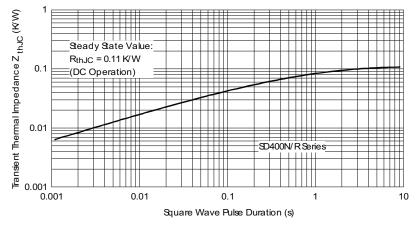
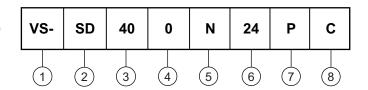


Fig. 8 - Thermal Impedance ZthJC Characteristic



### **ORDERING INFORMATION TABLE**

Device code



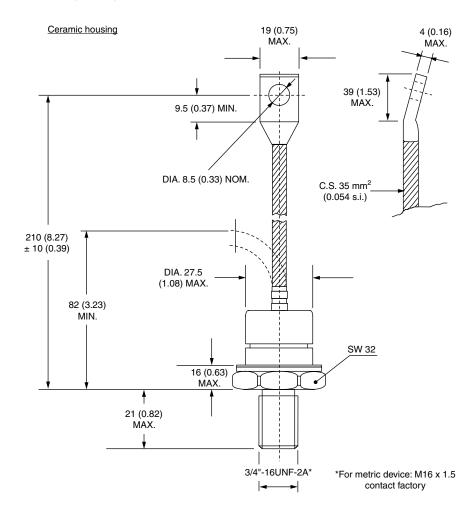
- 1 Vishay Semiconductors product
- 2 Diode
- 3 Essential part number
- 4 0 = standard recovery
- 5 • N = stud normal polarity (cathode to stud)
  - R = stud reverse polarity (anode to stud)
- 6 Voltage code x 100 = V<sub>RRM</sub> (see Voltage Ratings table)
- 7 P = stud base DO-9 (DO-205AB) 3/4" 16UNF-2A
- 8 C = ceramic housing

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95301			



# **DO-205AB (DO-9)**

### **DIMENSIONS** in millimeters (inches)





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